Procedures and criteria for evaluation and recognition of student's progress within course activities during the semester - Electronical Engineering and Telecomunications – Politehnica International Programme ( 4 year bachelor) - 2<sup>nd</sup> year

## **Study Program Missions**

Didactic Mission	Developing researcher skills and enhancing the competence as specialists by improving the students abilities in design, modeling, simulation, testing and implementation of analog and digital circuits, in exploiting signal processing techniques, in using computer languages and specialized programs to design electronic circuits starting from the technical specifications and to study their operations, in different conditions.
Research Mission	Developing abilities in analysing, in extracting technical specifications for a specific circuit design, in developing systems architecture and in modeling electronic circuits using iterative design, computer simulation and testing in real-time. Developing students skills for working in research teams.

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1.	II	Electronic Circuits	<ul> <li>Written examination</li> <li>2 internal supervisors;</li> <li>3 theoretical subjects +2 problems to solve (1 point d15from the start)</li> <li>The final grade is calculated as an average of the grade for the activity during the semester (1/3) and the grade for the exam (2/3), (rounded according to the RODPI of UPT);</li> <li>The grade 5 is obtained for promoting the exam with a grade higher or equal to 5 and obtaining a grade for the activity during the semester higher or equal to 5.</li> <li>The grade 10 is obtained for a grade for the exam higher or equal to 9 (if the grade for the activity during the semester is at least 9);</li> <li>Aditional bonus to the exam grade (up to 1 point) can be given, if supplementary tasks related to discipline are performed during semester.</li> <li>The exam is held in a classroom alocated by the faculty, acording to the total number of students to be examined.</li> </ul>	<ul> <li>Activity, exam and other test grades will be recognized until the student graduate the faculty, or the discipline changes the syllabus</li> </ul>
2.	II	Engineering	<ul> <li>Distributed examination, with two written papers (week 7 and week 13)</li> <li>Two internal supervisors;</li> </ul>	• The mark for the practic activity during the semester (N3), if it is equal to 5 or higher,

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		Electro- magnetics	<ul> <li>5 subjects with 3 short theoretic questions and two simple problems, for each written paper (two marks N1 and N2)</li> <li>Continuous practic and theorethic evaluation at lab (N3)</li> <li>The finale grade is calculated as sum (N1+N2+N3)/3 &gt;= 4,50</li> <li>The grade 5 is obtained for promoting lab activity, N3&gt;= 5, and, (N1+N2+N3)/3 &gt;= 4,50</li> <li>A good presence can be taken into account in the benefit of the student;</li> <li>The grade 10 for the final mark implies that at least one of the N1, N2 or N3 &gt;= 9.50 and the average (N1+N2+N3)/3 &gt; 9.00 and a very good presence at the activities. ;</li> <li>The written examination is held in an adequate classroom decided by the faculty's executive board.</li> </ul>	remains valid for the any future re- examinations (if required by the student). N1 and/or N2 if >= 4.5 remain valid till the end of the year, if required by the student.
3.	II	Signals and Systems	<ul> <li>Written, exam,</li> <li>4 teachers,</li> <li>5 theory questions and 2 exercises, each question or exercise is appreciated with a mark between 1 and 10,</li> <li>The final mark 5 is given if the sum of the marks obtained for the theory questions is higher than 25 and if the sum of the marks obtained for the two exercises is higher then 10. The date, the classroom and the starting hour are communicated in due time to the interested students</li> </ul>	• At each course are proposed homework subjects, at each lab, the students are examined.
4.	II	Digital Integrated Circuit	<ul> <li>Written examination</li> <li>2 internal supervisors;</li> <li>3 theoretical subjects, 3 applications, 1,5 points each;</li> <li>Exam pass requires 50% completition for theory and applications;</li> <li>The final grade is 67% the written examination and 33% the lab</li> <li>Homeworks: problemspresented each 2nd course;</li> <li>The quality of the essay and its presentation will be reflected in the grade for the activity during the semester; others issues reflected in the grade for the activity during the semester; the quality of answers and participation during the seminars.</li> <li>The grade 5 is obtained for promoting the exam with a grade higher or equal to 5 and obtaining a grade for the activity during the semester higher or equal to 5.</li> <li>The grade 10 is obtained for a grade for the exam higher or equal to 9 (if the grade for the activity during the semester is 10) and respectively for the grade 10 for the exam (when the grade for the activity during the semester is 9);</li> <li>The examination is held in the classrooms decided by the faculty's executive board.</li> </ul>	<ul> <li>Grades &gt; 5 for the exam and grades &gt; 5 for the activity during the semester are recognized unconditionally, at any moment.</li> <li>If the students consider it useful, a partial examination is established after the 7th week (and prior to the 11th week). T</li> </ul>
5.	II	Object Oriented Programming	<ul> <li>Multiple-choice final exam.</li> <li>Minimum 2 supervisors.</li> <li>The multiple choice test has 30 questions. Each question has 5 possible answers, only one correct.</li> <li>The mark 5 means 50% correct answers,</li> <li>The mark 10 means 100% correct answers.</li> <li>The mark from the multiple choice test represents 50% from the final mark. The multiple choice test will be held in a course room.</li> </ul>	• The marks equal or grater than 5 for the exam and for the laboratory activity will be recognized unconditionally at any moment.

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			<ul> <li>The mark for the laboratory activity will be the average of the practical tests during the semester and represents the 50% from the final mark.</li> <li>The final mark will be computed if the marks from the multiple choice and the laboratory are equal or grater than 5 and will be rounded according RODPI of UPT.</li> </ul>	
6.	II	Electrical and Electronic Measurements	<ul> <li>Written exam.</li> <li>Two internal examiners.</li> <li>A theoretical subject (20 mcqs) and 2 to 4 problems. Duration: 2,5h. Weight of activity mark in the final mark: 33%.</li> <li>A passing mark of 5 is obtained for solving half of the subjects, if the activity mark is at least 5.</li> <li>For the maximum mark (10), 90% of the exam subjects must be properly solved and the activity mark must be at least 9,5. Room assigned by the executive board of the faculty. Exam subjects multiplied for each student.</li> </ul>	<ul> <li>Marks of at least 5 (both for the "exam" and for the activity) are recognized unconditionally at any moment.</li> </ul>
7.	II	Second Language	<ul> <li>Distributed evaluation;</li> <li>Internal examiners;</li> <li>Final test represents 1/2 of the final mark; 3 practical questions;</li> <li>Mark 5 is given for proof of having acquired the language skills taught and using them at minimum level;</li> <li>Mark 10 is given for the full acquisition of the language skills taught and for proof of the ability to use them;</li> <li>Classroom alloted by the faculty</li> </ul>	• The marks obtained at the final test and for the activity during seminars are valid until the course is passed
8	II	Sport	<ul> <li>Collocutional exam.</li> <li>The titular professor assures the evaluation of the activity.</li> <li>The student must participate rithmically and actively to the lessons and competions organized in the Department of Physical Education and Sport</li> <li>The qualificative "Admited/Rejected" is conditioned by the 100% participation of the lessons.</li> </ul>	• During the semester, there are a few tests that stuents must fulfill. These tests are recognised and reflected in the final qualificative.
9	П	Practical Training	According to Practice Regulation	-
10	II	Analog Integrated Circuits	<ul> <li>Written examination,</li> <li>2 supervisors.</li> <li>10 questions concerning theoretical knowledge (1 point each, maximum score is 10 points) and 2 problems (the maximum score is 5 points each problem, the maximum score for problems is 10). The examination mark is the average of the two marks (for problems solutions and for the theory). Final mark: the weighted average of the examination mark and the lab activity: 2/3* examination mark + 1/3* lab activity, rounded according to the RODPI of UPT (i.e. 2/3 * 8,4 + 1/3 *9 means 9 as final mark for AIC).</li> <li>Mark 5 : minimum 5 for theoretical knowledge and minimum 5 for solutions to the problems and minimum 5 for lab activity.</li> <li>Mark 10 : the weighted average equal to or greater than 9.5.</li> </ul>	<ul> <li>During the lab activity, the teacher is testing the students (2 -3 written tests) on their knowledge of a lab experiment and also 2 tests during seminars. The average value of these 4 or 5 marks is the mark for lab activity.</li> </ul>

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11	Π	Microeconomics	<ul> <li>Written examination</li> <li>2 internal supervisors;</li> <li>6 theoretical subjects, one point each, and one application of 3 points (1 point from the start)</li> <li>Homework: one case-study – calculating some economic indicators for a real company;</li> <li>The grade for the activity during the semester is based on the case-study and on a test organized during the semester; also, the quality of answers and the level of implication during the seminars have an influence on the grade for the activity during the semester.</li> <li>The final grade is calculated as weighted average of the grade for the activity during the semester.</li> <li>The grade for the exam is not rounded (it is expressed as a real number, with 2 decimals; the grade for the activity during the semester is expressed without decimals.</li> <li>The grade 5 is obtained for promoting the semester higher or equal to 5.</li> <li>The grade 10 is obtained for a grade for the exam higher or equal to 9,2 (if the grade for the activity during the semester is 9);</li> <li>The examination is held in the classrooms decided by the faculty's executive board.</li> </ul>	<ul> <li>Grades 5 for the exam and grades 5 for the activity during the semester are recognized unconditionally, a t any moment.</li> <li>If the students consider it useful, a partial examination is established after the 7th week (and prior to the 11th week). The grade obtained for the partial examination will be considered, for the final exam, instead of the answers for the subjects from the first part of the discipline (the first 7 chapters). Therefore, if in the final exam there are subjects worth 5 (4) points, for those subjects the number of points will be the grade from the partial examination (with a grade higher than 5) and who are willing to increase their grade, partial examinations can be accepted for the students can answer only to the subjects referring to the part (the chapters) for which the initial answers were wrong/incomplete.</li> </ul>
12	П	Computer Networks Architecture	<ul> <li>Distributed evaluation, after 7 and 14 weeks, respectively. At each verification, there is a written exam consisting of two problems and two subjects from theory.</li> <li>The minimum grade for each part is 5 for theory and 5 for problems. The final grade is computed (part 1 + part 2 +laboratory) / 3. The exam subjects are printed for each student. In the first 10 minutes, the student can ask questions regarding the subjects and then the evaluation begins 1.5 hours per each part. The student can retake the exam first or second part, before the examination period begins.</li> <li>The maximum grade 10 is obtained if the mean computed, (P1+P2+L)/3 is minumum 9.5.</li> </ul>	<ul> <li>Progressive accumulation in the framework of the discipline are recognized by the mark obtained during the laboratory activities, with the weight of 1/3 in the final grade</li> </ul>
13	II	Computer Aided Design	<ul> <li>Distributed evaluation in two tests, each after 7 lectures.</li> <li>Minimum two course responsibles examiners (lecturer + lab assistant).</li> <li>The grades will count to final grade according to formula N= 0.5*L1 + 0.5*L2, and the fianl garde will consist off 0.5 from lecture tests and 0.5 from laboratory work.</li> <li>The tests will consist off 6-10 questions from lecture material, with a min off 40 % of it applicative oriented (software tools use).</li> <li>Digree 5 can be obtained after successfuly passing halfs of the test questions and completion of parctical requierments (laboratory work).</li> <li>Examination organized with help from deans office.</li> </ul>	<ul> <li>Evaluation following laboratory work comes with 'merit' bonuses for students completing assigmnets and delivery 'on time'.</li> <li>The laboratory work (hands on experience) counts as 1/2 of the final grade (valid for graduation requirement).</li> </ul>
14	II	Signal Processing	<ul> <li>Written, exam,</li> <li>4 teachers,</li> </ul>	<ul> <li>At each course are proposed homework subjects, at each lab, the students are examined.</li> </ul>

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			<ul> <li>5 theory questions and 2 exercises, each question or exercise is appreciated with a mark between 1 and 10,</li> <li>The final mark 5 is given if the sum of the marks obtained for the theory questions is higher than 25 and if the sum of the marks obtained for the two exercises is higher then 10. The date, the classroom and the starting hour are communicated in due time to the interested students</li> </ul>	
15	II	Microprocessors and Microcontrollers	<ul> <li>Written examination.</li> <li>Number of examinators :2.</li> <li>Examen content : 2 subjects with theoretical and applicative questions and 1 subject with a problem. Notation criteria : the notation is made upon the number of the achieved points by answering to the theoretical questions and solving the applicative questions and the problem.</li> <li>Minimal condition for obtaining the mark 5: to achieve 45% of the maximum number of points.</li> <li>Minimal condition to obtain the mark 10: to achieve 95% of the maximum number of points.</li> </ul>	• The progressive accumulations of knowledges in the field are recognised through the laboratory activity evaluation mark and the marks at the tests given during the semester, with 1/2 share of the final mark.
16	II	Engineering Ethics and Communication	<ul> <li>Continuous assessment,</li> <li>Internal examiners,</li> <li>Final written examen represents 1/2 of the final mark; 3 questions; 1 theretical and 2 practical;</li> <li>Mark 5 corrresponds to a minimum level of knowledge and skills;</li> <li>Mark 10 corrresponds to a complete mastering of theoretical knowledge and ability to put it into practice;</li> <li>Classroom for examination is allotted by the Faculty</li> </ul>	<ul> <li>The marks obtained at the final test and for the activity during seminars are valid until the course is passed</li> </ul>
17	II	Electronic Circuits Project	<ul> <li>Practica project, distributed examination, oral,</li> <li>A PCB soldered with components is required for 5 grade and a fully functional PCB is required for grade 10</li> </ul>	<ul> <li>Well defined homeworks (schematic entry, BOM, PCB design, simulation, computations for power and cost)</li> </ul>
18	П	Sport	<ul> <li>Collocutional exam.</li> <li>The titular professor assures the evaluation of the activity.</li> <li>The student must participate rithmically and actively to the lessons and competions organized in the Department of Physical Education and Sport</li> <li>The qualificative "Admited/Rejected" is conditioned by the 100% participation of the lessons.</li> </ul>	<ul> <li>During the semester, there are a few tests that stuents must fulfill. These tests are recognised and reflected in the final qualificative.</li> </ul>
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