

Lista lucrărilor

1. Lista lucrărilor relevante (toate sunt publicate în reviste ISI străine):

1. D. Belega, D. Petri, "Accuracy Analysis of the Multicycle Synchrophasor Estimator Provided by the Interpolated DFT Algorithm," *IEEE Trans. Instrum. Meas.*, vol. 62, no. 5, pp. 942-953, May, 2013, ISSN 0018-9456, DOI: 10.1109/TIM.2012.223677
2. D. Belega, D. Petri, "Statistical Performance of the Effective-Number-of-Bits Estimators Provided by the Sine-Fitting Algorithms," *IEEE Trans. Instrum. Meas.*, vol. 62, no. 3, pp. 633-640, March, 2013, ISSN 0018-9456, DOI: 10.1109/TIM.2012.2218679
3. D. Belega, D. Petri, D. Dallet, "Noise Power Estimation by the Three-Parameter and Four-Parameter Sine-Fit Algorithms," *IEEE Trans. Instrum. Meas.*, vol. 61, no. 12, pp. 3234-3240, Dec., 2012, ISSN 0018-9456, DOI: 10.1109/TIM.2012.2205511
4. D. Belega, D. Dallet, D. Petri, "Accuracy of the Normalized Frequency Estimation of a Discrete-Time Sine-Wave by the Energy-Based Method," *IEEE Trans. Instrum. Meas.*, vol. 61, no. 1, pp. 111-121, Jan, 2012, ISSN 0018-9456, DOI: 10.1109/TIM.2011.2159318
5. D. Belega, D. Dallet, D. Petri, "Statistical Description of the Sine-Wave Frequency Estimator Provided by the Interpolated DFT Method," *Measurement*, vol. 51, no. 1, pp. 109-117, Jan., 2012, ISSN 0263-224, DOI: 10.1016/j.measurement.2011.09.010
6. D. Belega, D. Dallet, D. Petri, "A High-Performance Procedure for Effective Number of Bits Estimation in Analog-to-Digital Converters," *IEEE Trans. Instrum. Meas.*, vol. 60, no. 5, pp. 1522-1532, May 2011, ISSN 0018-9456, DOI: 10.1109/TIM.2010.2089151
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8. D. Belega, D. Dallet, "Amplitude Estimation by a Multipoint Interpolated DFT Approach," *IEEE Trans. Instrum. Meas.*, vol. 58, no. 5, pp. 1316-1323, May 2009, ISSN 0018-9456, DOI: 10.1109/TIM.2009.2012950
9. D. Belega, D. Dallet, "Multifrequency Signal Analysis by Interpolated DFT Method with Maximum Sidelobe Decay Windows," *Measurement*, vol. 42, no. 3, pp. 420-426, April 2009, ISSN 0263-2241, DOI: 10.1016/j.measurement.2008.08.006
10. D. Belega, D. Dallet, "Frequency Estimation Via Weighted Multipoint Interpolated DFT," *IET Science, Measurement & Technology*, vol. 2, no 1, pp. 1-8, 2008, ISSN 1751-8822, DOI: 10.1049/iet-smt:20070022

2. Teza de doctorat:

Daniel Belega, "Contribuții la testarea convertoarelor analog-numerice", Universitatea "Politehnica" din Timișoara, conducător științific: prof. dr. ing. Mircea Ciugudean, 2001.

3. Capitole în cărți (Edituri din străinătate):

1. D. Petri, D. Belega, D. Dallet, *Dynamic testing of analog-to-digital converters by means of the sine-fitting algorithms*, in the book Design, Modeling, and Testing of Data Converters, Springer Verlag, Germany, in press.

4. Cărți (Edituri din România):

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3. D. Belega, C. Dughir, R. Pazsitka, *Circuite integrate analogice. Experimente și simulare*, Editura Politehnica din Timișoara, 2008, 75 p., ISBN 978-973-625-639-4
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5. D. Belega, *Testarea dinamică a convertoarelor analog-numerice*, Editura Politehnica din Timișoara, 2004, 159 p., ISBN 973-625-130-6
6. D. Belega, *Placa de dezvoltare TMS320C5x în aplicații*, Editura Politehnica din Timișoara, 2002, 156 p., ISBN 973-625-002-4

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18. D. Belega, M. Ciugudean, D. Stoiciu, "Choice of the Cosine-Class Windows for ADC Dynamic Testing by Spectral Analysis," *Measurement*, vol. 40, no. 4, pp. 361-371, 2007, ISSN 0263-2241, DOI: 10.1016/j.measurement.2006.06.016
19. D. Belega, D. Dallet, "Dynamic Testing of A/D Converters by Means of the Three-Parameter Sine-Fit Algorithm," *Measurement*, vol. 40, no. 1, pp. 1-7, 2007, ISSN 0263-2241, DOI: 10.1016/j.measurement.2006.05.013

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6. D. Belega, "Optimal Choice of the Windows for ADC Characterization by Spectral Analysis in Single-Tone and Dual-Tone Modes," *Revue Roumaine des Sciences Techniques Serie Électrotechnique et Énergétique*, vol. 49, no.1, pp. 75-84, Janvier-Mars, 2003, ISSN 0035-4066.
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6. D. Belega, D. Petri, D. Dallet, "An Effective Procedure for the Estimation of Harmonic Parameters of Distorted Sine-Waves," *Proceedings of the I2MTC IEEE International Instrumentation and Measurement Technology Conference (IMTC)*, pp. 1429-1434, Graz, Austria, May 13-16, 2012, ISSN 1091-5281, DOI: 10.1109/I2MTC.2012.6229677
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9. D. Petri, D. Belega, "Transient Behavior of the Full-Cycle DFT Phasor Estimator," *Proceedings of the IEEE Workshop on Applied Measurements for Power Systems (AMPS)*, pp. 31-36, Aachen, Germany, September 28-30, 2011, ISBN 978-1-61284-946-1, DOI: 10.1109/AMPS.2011.6090436
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11. D. Belega, D. Petri, D. Dallet, "Sine-Fitting by the Energy-Based Method in the Dynamic Testing of ADCs," *Proceedings of the IEEE Conference on Intelligent Data Acquisition and Advanced Computing Systems: Technology and Applications (IDAACS)*, vol. I, pp. 33-38, Prague, Czech Republic, September 15-17, 2011, ISBN 978-1-4577-1426-9, DOI: 10.1109/IDAACS.2011.6072706
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