

FIȘĂ DE ÎNDEPLINIRE A STANDARDELOR MINIMALE

Conf. dr. ing. MIHAI UDRESCU-MILOSAV

RAMURA DE ȘTIINȚĂ: CALCULATOARE, TEHNOLOGIA INFORMAȚIEI ȘI INGINERIA SISTEMELOR

STANDARDE MINIMALE ȘI OBLIGATORII PENTRU CONFERIREA TITLURILOR DIDACTICE DIN ÎNVĂȚĂMÂNTUL SUPERIOR ȘI A GRADELOR PROFESIONALE DE CERCETARE-DEZVOLTARE

1. Structura activității candidatului								
Nr. crt.	Domeniul activităților	Categoriile și restricții		Subcategoriile		Indicatori kpl	Nr. Realizat	Punctaj
0	1	2		3		4	5	6
1	Activitatea didactică și profesională (A1)	Cărți de autor sau capitole de specialitate la edituri cu ISBN	Cărți/monografii	A 1.1.1	Inter naționale	50/nr. de autori	1 capitol, 50/3	16.66
				A 1.1.2	Naționale	50/nr. de autori	2 cărți, 50/1 +50/2	75
		Material didactic/Lucrări didactice publicate la edituri cu ISBN	Manuale didactice	A 1.2.1		40/nr. de autori	1 lucrare didactică, 40/3	13.33
							TOTAL A1	105
2	Activitatea de cercetare (A2)	Articole în reviste cotate ISI și lucrări în volumele unor manifestări științifice indexate ISI		A 2.1		(25+30* factor impact)/ nr. de autori	39	431.17
		Articole în reviste și lucrări în volumele unor manifestări științifice indexate în alte baze de date internaționale recunoscute (BDI)		A 2.2		20/nr. de autori	21	115.22
		Proprietate intelectuală,		A 2.3.1	Inter naționale	35/ nr. de autori		

		brevete de invenție, certificate ORDA		A 2.3.2	Naționale (OSIM)	25/ nr. de autori		
		Granturi/proiecte de cercetare câștigate prin competiție sau contracte cu agenți economici în valoare de minimum 10000 dolari SUA echivalent încasați	Director/re sponsabil partener	A 2.4.1.1	Inter naționale	20*ani de desfășurare		
				A 2.4.1.2	Naționale	10*ani de desfășurare	2 granturi* 2 ani	40
			Membru în echipă	A 2.4.2.1	Inter naționale	4*ani de desfășurare	2 granturi* 2 ani + 1 grant* 1 an	20
				A 2.4.2.2	Naționale	2*ani de desfășurare	2 granturi* 3 ani	12
							TOTAL A2	618.39
3	Recunoașterea și impactul activității (A3)	Citări în cărți, reviste și volume ale unor manifestări științifice		A 3.1.1	Cărți, ISI	(8/nr. aut art. citat)*2 dacă art care citează este în Q1, Q2	45	137.067
				A.3.1.2	BDI	4/nr. aut art. citat	14	15.6
		Membru în comitetele de redacție sau comiteele științifice ale revistelor indexate ISI, chair, co-chair sau membru în comitetele de organizare ale manifestărilor		A 3.2		10	4 Comitete conferințe * 10	40
		Membru în comitetele de redacție sau comiteele științifice ale revistelor indexate BDI, chair, co-chair sau membru în comitetele de		A 3.3		6	1 Comitet conferință * 6	6

		organizare ale manifestărilor						
		Premii în domeniu conferite de Academia Română, ASTR, AOSR, sau premii internaționale de prestigiu		A 3.4		15	1	15
							TOTAL A3	213.67
						TOTAL A1+A2+A3	937.06	

Condiții minimale pentru profesor/abilitare			
3. Condiții minimale			
Nr. crt	Domeniul de activitate	Profesor / abilitare	Punctaj realizat
1	Activitatea didactică/profesională (A1)	100	105
2	Activitatea de cercetare (A2)	600	618.39
3	Recunoașterea impactului activității (A3)	150	213.67
Total A		850	937.06
Condiții minimale obligatorii pe subcategorii		Profesor / abilitare	Realizat
A 1.1.1 - A 1.1.2	Cărți de specialitate	1 carte	3 cărți, 1 capitol
A 2.1	Articole în reviste cotate ISI și lucrări în volumele unor manifestări științifice indexate ISI proceedings	15, din care minimum 3 în reviste cotate Q1 sau Q2	39, din care 3 în reviste Q1 sau Q2
A 2.4.1	Granturi/proiecte de cercetare câștigate prin competiție (director/responsabil partener)	2	2
A 3.1.1	Număr de citări în cărți, reviste cotate ISI și volume ale unor manifestări științifice ISI (WoS)	25	43
	Factor de impact ISI cumulată pentru publicații	10	20.117

A 1 Activitatea didactică și profesională								
A 1.1 Cărți de autor sau capitole de specialitate la edituri cu ISBN								
A 1.1.1 Internaționale								
Nr. Crt	Autori	Titlul cărții	Editura	ISBN	Anul	Număr de pagini	Număr de autori	Punctaj
1	Alexandru Topîrceanu, Mihai Udrescu, Mircea Vlăduțiu	Chapter "Genetically Optimized Realistic Social Network Topology Inspired by Facebook" in Lecture Notes in Social Networks, Online Social Media Analysis and Visualization, Jalal Kawash (Ed.), pp. 163-179	Springer	ISBN 978-3-319-13590-8	2014	17	3	16.67
							TOTAL	16.67
A 1.1.2 Naționale								
Nr. Crt	Autori	Titlul cărții	Editura	ISBN	Anul	Număr de pagini	Număr de autori	Punctaj
1	Mihai Udrescu, Lucian Prodan	Emerging Computing Systems: Quantum Computing From a Computer Engineering Perspective	Editura Politehnica Timișoara	ISBN 978-606-554-684-4	2013	128	2	25.00
2	Mihai Udrescu	Quantum Circuits Engineering	Editura Politehnica Timișoara	ISBN 978-973-625-815-2	2009	227	1	50.00
							TOTAL	75.00
A 1.2.1 Material didactic/Lucrări didactice publicate la edituri cu ISBN: Manuale didactice								
Nr. Crt	Autori	Titlul cărții	Editura	ISBN	Anul	Număr de pagini	Număr de autori	Punctaj
1	Flavius Oprițoiu, Lucian Prodan, Mihai Udrescu	Aritmetica în sistemele de calcul: probleme de design și implementare	Editura Politehnica Timișoara	ISBN 978-606-554-566-3	2013	136	3	13.33
							TOTAL	13.33
						TOTAL A1	105	

A2 Activitatea de cercetare				
A 2.1 Articole în reviste cotate ISI și lucrări în volumele unor manifestări științifice indexate ISI				
Nr. crt.	Lucrarea publicată	Nr. Autori	Impact Factor	Punctaj
1	Lucretia Udrescu, Laura Sbarcea, Alexandru Topirceanu, Alexandru Iovanovici, Ludovic Kurunczi, Paul Bogdan, <u>Mihai Udrescu</u> , "Clustering drug-drug interaction networks with energy model layouts: community analysis and drug repurposing", Nature Scientific Reports 6:32745, 2016, ISSN 2045-2322 (corresponding and coordinating author)	7	5.228	25.97714286
2	Alexandru Topirceanu, Alexandra Duma, <u>Mihai Udrescu</u> , "Uncovering the Fingerprint of Online Social Networks Using A Network Motif Based Approach", Computer Communications, 73(PartB), pp. (167–175), 2016, Elsevier, ISSN: 0140-3664.	3	2.099	29.32333333
3	Liana Suciu, Carmen Cristescu, Alexandru Topirceanu, Lucreția Udrescu, <u>Mihai Udrescu</u> , Valentina Buda, Mirela Cleopatra Tomescu "Evaluation of patients diagnosed with essential arterial hypertension through network analysis", Irish Journal of Medical Science, 185(2), pp. (443–451), 2016, Springer, ISSN: 0021-1265.	7	1.158	8.534285714
4	Alexandru Topirceanu, <u>Mihai Udrescu</u> , "Statistical Fidelity: A Tool to Quantify the Similarity Between Multi-variable Entities with Application in Complex Networks", International Journal of Computer Mathematics, 0(0), 2016, ISSN, 0020-7160	2	0.577	21.155
5	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Simulated Fault Injection Methodology for Gate-Level Quantum Circuit Reliability Assessment", Simulation Modelling Practice and Theory, 23(1), pp. (60-70), 2012, Elsevier, ISSN: 1569-190X.	3	1.159	19.92333333
6	Oana Boncalo, Alexandru Amaricai, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Quantum Circuit's Reliability Assessment with VHDL-Based Simulated Fault Injection", Microelectronics Reliability, 50(2), pp. (304-311), 2010, Elsevier, ISSN 0026-2714.	4	1.137	14.7775
7	Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo, Mircea Vladutiu, "Design for Dependability in Emerging Technologies", ACM Journal of Emerging	4	0.759	11.9425

	Technologies in Computing, 3(2):Article 6, ACM, 2007, ISSN 1550-4832.			
8	Cristian Cosariu, Alexandru Iovanovici, Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Bio-inspired redistribution of urban traffic flow using a social network approach", IEEE Congress on Evolutionary Computation, Sendai, Japan, 25-28 May, 2015, pp. 77-84, ISBN 978-1-4799-7492-4.	5	0.25	6.5
9	Alexandru Topîrceanu, <u>Mihai Udrescu</u> , "FMNet: Physical Trait Patterns in the Fashion World", 2nd European Network Intelligence Conference, ENIC, Karlskrona, Sweden, 21-22 Sep, 2015, pp. 25-32, ISBN 978-1-4673-7592-4	2	0.25	16.25
10	Alexandru Topîrceanu, <u>Mihai Udrescu</u> , "Measuring Realism of Social Network Models Using Network Motifs", 10th Jubilee IEEE International Symposium on Applied Computational Intelligence and Informatics, May 21-23, 2015 Timisoara, Romania, pp.443-447, 2015, ISBN 978-1-4799-9911-8.	2	0.25	16.25
11	<u>Mihai Udrescu</u> , Alexandru Topirceanu, "What drives the emergence of social networks?", 20th International Conference on Control Systems and Computer Science (CSCS), Bucharest, Romania, 27-29 May, 2015, pp. 999-999, ISBN 978-1-4799-1779-2	2	0.25	16.25
12	Alexandru Topirceanu, Dragos Tiselice, <u>Mihai Udrescu</u> , "The Fingerprint of Educational Platforms in Social Media: A Topological Study Using Online Ego-Networks", International Conference on Social Media in Academia: Research and Teaching (SMART), Timisoara, Romania, 18-21 Sep, 2014, pp. (355-360), ISBN 978-88-7587-712-5	3	0.25	10.83333333
13	Alexandru Topirceanu, Cezar Fleseriu, <u>Mihai Udrescu</u> , "Gamified: An Effective Approach to Student Motivation Using Gamification", International Conference on Social Media in Academia: Research and Teaching (SMART), Timisoara, Romania, 18-21 Sep, 2014, pp. (41-44), ISBN 978-88-7587-712-5	3	0.25	10.83333333
14	Alexandru Topirceanu, Gabriel Barina, <u>Mihai Udrescu</u> , "MuSeNet: Collaboration in the music artists industry", 1st European Network Intelligence Conference, ENIC, Wroclaw, Poland, 29-30 Sep, 2014, pp. (89-94), ISBN: 978-147996914-2.	3	0.25	10.83333333

15	Gabriel Barina, Alexandru Topirceanu, <u>Mihai Udrescu</u> , "MuSeNet: Natural Patterns in the Music Artists Industry", 9th IEEE International Symposium on Applied Computational Intelligence and Informatics (SACI), Timisara, Romania, 15-17 May, 2014, pp. (317-322), ISBN:978-1-4799-4694-5.	3	0.25	10.83333333
16	Alexandru Topirceanu, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Network Fidelity: A Metric to Quantify the Similarity and Realism of Complex Networks", 3rd IEEE International Conference on Cloud and Green Computing (CGC), Karlsruhe, Germany, Sep 30-Oct 02, 2013, pp. (289-296), ISBN 978-0-7695-5114-2.	3	0.25	10.83333333
17	Liviu Agnola, Mircea Vladutiu, <u>Mihai Udrescu</u> , Lucian Prodan, "Improving performance of robust Self Adaptive Caches by optimizing the switching algorithm", IEEE 14th International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS), Cottbus, Germany, 13-15 Apr, 2010, pp. (297-300), ISBN 978-1-4244-9753-9.	4	0.25	8.125
18	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Performance Analysis for Genetic Quantum Circuit Synthesis", Proceedings Artificial Intelligence and Soft Computing (ICAISC 2010), Lecture Notes in Computer Science (Springer), vol. 6114, Zakopane, Poland, 13 - 17 Jun, 2010, pp. (205-212), ISBN 978-3-642-13231-5.	4	0.25	8.125
19	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Adaptive vs. Self-Adaptive Parameters for Evolving Quantum Circuits", 9th International Conference on Evolvable Systems: From Biology to Hardware (ICES 2010), York, UK, Lecture Notes in Computer Science (Springer), vol. 6274, 6-8 Sep, 2010, pp. (348-359), ISSN: 0302-9743.	4	0.25	8.125
20	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Genetic Algorithm Based Quantum Circuit Synthesis with Adaptive Parameters Control", IEEE Congress on Evolutionary Computation (CEC 2009), Trondheim, Norway, 18-21 May 2009, pp. (896-903), ISBN 978-1-4244-2959-2.	4	0.25	8.125
21	Flavius Opritoiu, Mircea Vladutiu, <u>Mihai Udrescu</u> , Lucian Prodan, "Round-Level Concurrent Error Detection Applied to Advanced Encryption	4	0.25	8.125

	Standard", 12th IEEE Symposium on Design and Diagnostics of Electronic Systems, Liberec, Czech Republic, April 15-17, 2009, pp. (270-275), ISBN 978-1-4244-3339-1.			
22	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Quantum Circuit Synthesis with Adaptive Parametres Control", EuroGP2009. European Conference on Genetic Programming (Springer-Verlag Berlin Heidelberg, LNCS), LNCS 5481, Tubingen, Germany, 15-17 Apr, 2009, pp. (339-350), ISBN 978-3-642-01180-1.	4	0.25	8.125
23	Flavius Opritoiu, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , "Built-In Self Test Applicability for the Non-Linear Operations of Advanced Encryption Standard", 5th International Symposium on Applied Computational Intelligence and Informatics, Timisoara, Romania, May 28-29, 2009, pp. (307-312), ISBN: 978-1-4244-4478-6.	4	0.25	8.125
24	Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Fault-Tolerant Memory Design and Partitioning Issues in Embryonics", Evolvable Systems: From Biology to Hardware. LNCS5216, 8th International Conference ICES, Prague, Czech Republic, 21-24 Sep, 2008, pp. (372-381), ISSN: 0302-9743.	3	0.25	10.83333333
25	Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo, "Floating Point Multiplication Rounding Schemes for Interval Arithmetic", Proceedings 19th IEEE Conference on Application Specific Systems, Architectures and Processors (ASAP), Leuven, Belgium, 2-4 Jul, 2008, pp. (19-24), ISBN: 978-1-4244-1898-5.	5	0.25	6.5
26	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "A Genetic Algorithm Framework Applied to Quantum Circuit Synthesis", iInternational Workshop on Nature Inspired Cooperative Strategies for Optimization (NICSO'07), Acireale, Italy, 8-10 Nov, 2007, pp. (419-429), ISBN: 978-3-540-78986-4.	4	0.25	8.125
27	Oana Boncalo, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, Alexandru Amaricai, "Saboteur Based Fault Injection for Quantum Circuits Fault Tolerance Assessment", Proceedings 10th EUROMICRO Conference on Digital System Design, Architectures, Methods and Tools (DSD 2007),	5	0.25	6.5

	Lübeck, Germany, 29-31 Aug, 2007, pp. (634-640), ISBN: 0-7695-2978-X.			
28	Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo, "Exploiting Parallelism in Double Path Adders' Structure for Increased Throughput of Floating Point Addition", Proceedings 10th EUROMICRO Conference on Digital System Design, Architectures, Methods and Tools (DSD 2007), Lübeck, Germany, 29-31 Aug, 2007, pp. (132-137), ISBN: 0-7695-2978-X.	5	0.25	6.5
29	Oana Boncalo, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, Alexandru Amaricai, "Assessing Quantum Circuits Reliability with Mutant-Based Simulated Fault Injection", Proceedings 18th European Conference on Circuit Theory and Design (ECCTD'07), Seville, Spain, 26-30 Aug, 2007, pp. (942-945), ISBN: 1-4244-1342-7.	5	0.25	6.5
30	Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo, "Hardware Support for Combined Interval and Floating Point Multiplication", Proceedings 14th Mixed Design Of Integrated Circuits and Systems, Ciechocinek, Poland, 21-23 Jun, 2007, pp. (278-282), ISBN: 83-922632-4-3.	5	0.25	6.5
31	Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo, "Design of Addition and Multiplication Units for High Performance Interval Arithmetic Processor", IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems, Krakow, Poland, 11-13 Apr, 2007, pp. (223-226), ISBN: 1-4244-1161-0.	5	0.25	6.5
32	Oana Boncalo, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, Alexandru Amaricai, "Using Simulated Fault Injection for Fault Tolerance Assessment of Quantum Circuits", Proceedings IEEE 40th Annual Simulation Symposium, Norfolk VA, USA, 26-28 Mar, 2007, pp. (213-220), ISSN: 1080-241X, ISBN: 0-7695-2814-7.	5	0.25	6.5
33	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Automatic Synthesis for Quantum Circuits using Genetic Algorithms", Proceedings ICANNGA'07 "Adaptive and Natural Computing Algorithms", LNCS 4431 (Springer -Verlag Berlin Heidelberg), Warsaw, Poland, 11-14 Apr, 2007, pp. (174-183), ISBN: 978-3-540-71589-4.	4	0.25	8.125

34	Oana Boncalo, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, Alexandru Amaricai, "Simulated Fault Injection for Quantum Circuits Based on Simulator Commands", 4th International Symposium on Applied Computational Intelligence and Informatics (SACI), Timisoara, Romania, 17-18 May, 2007, pp. (245-250), ISBN: 1-4244-1234-X.	5	0.25	6.5
35	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Simulated Fault Injection in Quantum Circuits with the Bubble Bit Technique", in Proceedings ICANNGA, Springer Adaptive and Natural Computing Algorithms, Coimbra, Portugal, 21-23 Mar, 2005, pp. (276-279), ISBN: 3-211-24934-6.	3	0.25	10.83333333
36	Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Multiple-Level Concatenated Coding in Embryonics: A Dependability Analysis", Proceedings GECCO (ACM / SIGEVO), Washigton DC, USA, 25-29 Jun, 2005, pp. (941-948), ISBN: 1-59593-010-8.	3	0.25	10.83333333
37	Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Survivability of Embryonic Memories: Analysis and Design Principles", Proceedings IEEE NASA/DoD Conference on Evolvable Hardware (EH'05), Washington DC, USA, 29-30 Jun, 2005, pp. (280-289), ISBN: 0-7695-2399-4.	3	0.25	10.83333333
38	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "The Bubble Bit Technique as Improvement of HDL-Based Quantum Circuits Simulation", Proceedings IEEE 38th Annual Simulation Symposium, San Diego CA, USA, 2-8 Apr, 2005, pp. (217-224), ISBN: 0-7695-2322-6.	3	0.25	10.83333333
39	Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Self-Repairing Embryonic Memory Arrays", Proceedings IEEE NASA/DoD Conference on Evolvable Hardware (EH'04), Seattle WA, USA, 24-26 Jun, 2004, pp. (130-137), ISBN: 0-7695-2145-2.	3	0.25	10.83333333
		TOTAL	20.117	431.1747619
Articole ISI în Q1 sau Q2				
Nr. crt.	Lucrearea publicată	Q1/Q2	Domain	
1	Lucretia Udrescu, Laura Sbarcea, Alexandru Topirceanu, Alexandru Iovanovici, Ludovic Kurunczi, Paul Bogdan, <u>Mihai Udrescu</u> , "Clustering drug-drug interaction networks with energy model layouts: community analysis and drug repurposing", Nature Scientific Reports 6:32745, 2016, ISSN 2045-2322 (corresponding and coordinating author)	Q1	Multidisciplinary Sciences	

2	Alexandru Topirceanu, Alexandra Duma, <u>Mihai Udrescu</u> , "Uncovering the Fingerprint of Online Social Networks Using A Network Motif Based Approach", Computer Communications, 73(PartB), pp. (167–175), 2016, Elsevier, ISSN: 0140-3664.	Q2	Computer Science, Information Systems
3	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Simulated Fault Injection Methodology for Gate-Level Quantum Circuit Reliability Assessment", Simulation Modelling Practice and Theory, 23(1), pp. (60-70), 2012, Elsevier, ISSN: 1569-190X.	Q2	Computer Science, Software Engineering

A 2.2 Articole în reviste și lucrări în volumele unor manifestări științifice indexate în alte baze de date internaționale recunoscute (BDI)

Nr. crt.	Lucrarea publicată	Nr. autori	BDI	Punctaj
1	<u>Mihai Udrescu</u> , Alexandru Topirceanu, "Probabilistic Modeling of Tolerance-based Social Network Interaction", 3rd European Network Intelligence Conference, ENIC, Wroclaw, Poland, 5-7 Sep, 2016, pp. (48 - 54), ISBN: ISBN-13: 978-1-5090-3455-0	2	IEEE Xplore	10
2	Alexandru Topirceanu, Jorge Garcia, <u>Mihai Udrescu</u> , "UPT.Social: The Growth of a New Online Social Network", 3rd European Network Intelligence Conference, ENIC, Wroclaw, Poland, 5-7 Sep, 2016, pp. (9 - 16), ISBN: ISBN-13: 978-1-5090-3455-0	3	IEEE Xplore	6.666666667
3	Alexandru Topirceanu, <u>Mihai Udrescu</u> , Mircea Vladutiu, Radu Marculescu, "Tolerance-based interaction: a new model targeting opinion formation and diffusion in social networks", PeerJ Computer Science 2:e42, 2016	4	Scopus	5
4	R. Pleava, D. Gaita, C. Ardeleanu, S. Frentz, <u>Mihai Udrescu</u> , L. Udrescu, R. Dan, D. Reisz, S. Mihaicuta. "Obesity in association with Sleep Apnea Syndrome as predictor for coronary-vascular comorbidities", Pneumologia 65(1):14-18, 2016	9	Scopus	2.222222222
5	Alexandru Topîrceanu, <u>Mihai Udrescu</u> , Răzvan Avram, Stefan Mihăicuță, "Data Analysis for Patients with Sleep Apnea Syndrome: A Complex Network Approach", V.E. Balas et al. (eds.), Soft Computing Applications, Advances in Intelligent Systems and Computing 356, Springer, pp. (231-239), 2016, ISBN 978-3-319-18296-4	4	SpringerLink	5
6	Alexandru Topîrceanu, Alexandru Iovanovici, Cristian Coșariu, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vlăduțiu, "Social Cities: Redistribution of Traffic Flow in Cities Using a Social Network Approach", V.E. Balas et al. (eds.), Soft Computing	6	SpringerLink	3.333333333

	Applications, Advances in Intelligent Systems and Computing 356, Springer, pp. (39-49), 2016, ISBN 978-3-319-18296-4			
7	Alexandru Iovanovici, Alexandru Topîrceanu, <u>Mihai Udrescu</u> , Mircea Vlăduțiu, "Heuristic Optimization of Wireless Sensor Networks using Social Network Analysis", V.E. Balas et al. (eds.), Soft Computing Applications, Advances in Intelligent Systems and Computing 356, Springer, pp. (663-671), ISBN 978-3-319-18296-4	4	SpringerLink	5
8	Alexandru Topirceanu, Alexandru Iovanovici, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Social cities: Quality assessment of road infrastructures using a network motif approach", 18th International Conference System Theory, Control and Computing (ICSTCC), Sinaia, Romania, 17-19 Oct, 2014, pp. (803-808), ISBN 978-1-4799-4602-0	4	IEEE Xplore	5
9	Alexandru Iovanovici, Alexandru Topirceanu, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Design space exploration for optimizing wireless sensor networks using social network analysis", 18th International Conference System Theory, Control and Computing (ICSTCC), Sinaia, Romania, 17-19 Oct, 2014, pp. (815-820), ISBN 978-1-4799-4602-0	4	IEEE Xplore	5
10	Alexandru Iovanovici, Alexandru Topirceanu, <u>Mihai Udrescu</u> , Lucian Prodan, Stefan Mihaicuta, "A high-availability architecture for continuous monitoring of sleep disorders", Studies in health technology and informatics Volume: 210, pp. 729-733, 2015, ISSN:0926-9630	5	Scopus	4
11	Liviu Agnola, Mircea Vladutiu, <u>Mihai Udrescu</u> , Lucian Prodan, "Simplified selective fault tolerance technique for protection of selected inputs via triple modular redundancy", 7th IEEE International Symposium on Applied Computational Intelligence and Informatics (SACI), Timisoara, Romania, 24-26 May, 2012, pp. (267-272), ISBN: 978-1-4673-1013-0	4	IEEE Xplore	5
12	Liviu Agnola, Mircea Vladutiu, <u>Mihai Udrescu</u> , Lucian Prodan, "Improving performance and area overhead by reducing the switching table for Self Adaptive cache Memories", IEEE 17th International Symposium for Design and Technology in Electronic Packaging (SIITME), Timisoara, Romania, 20-23 Oct, 2011, pp. (173-178), ISBN: 978-1-4577-1276-0	4	IEEE Xplore	5

13	Flavius Opritoiu, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , "A High-Speed AES Architecture Implementation", 7th ACM International Conference on Computing Frontiers (CF'10), Bertinoro, Italy, 17-19 May, 2010, pp. (95-96), ISBN 978-1-4503-0044-5.	4	ACM, DBLP	5
14	Flavius Opritoiu, Mircea Vladutiu, <u>Mihai Udrescu</u> , Lucian Prodan, "Concurrent Error Detection for Multiplicative Inversion of Advanced Encryption Standard", 10th IEEE International Conference on Computer and Information Technology (CIT 2010), Bradford, UK, 29-30 Jun, 2010, pp. (582-588), ISBN: 978-0-7695-4108-2	4	Scopus	5
15	Liviu Agnola, Mircea Vladutiu, <u>Mihai Udrescu</u> , "Self-Adaptive mechanism for cache memory reliability improvement", IEEE 13th International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS), Vienna, Austria, 14-16 Apr, 2010, pp. (117-118), ISBN 978-1-4244-6610-8	3	IEEE Xplore	6.666666667
16	Oana Boncalo, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, Alexandru Amaricai, "Error-Model Driven Analysis of Quantum Circuit's Reliability", Proceedings 8th IEEE Conference on Nanotechnology, Arlington, USA, August 18-21, 2008, pp. (103-106), ISBN 978-1-4244-2104-6	5	IEEE Xplore	4
17	Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu, "A Dependability Perspective on Emerging Technologies", 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, 2-5 May, 2006, pp. (187-198), ISBN 1-59593-302-6	3	INSPEC	6.666666667
18	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Implementing Quantum Genetic Algorithms: A Solution Based on Grover's Algorithm", 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, 2-5 May, 2006, pp. (71-82), ISBN 1-59593-302-6	3	INSPEC	6.666666667
19	Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Reliability Assessment in Embryonics Inspired by Fault-Tolerant Quantum Computation", Proceedings 2nd ACM International Conference on Computing Frontiers (CF'05), Ischia, Italy, 4-6 May, 2005, pp. (323-333), ISBN: 1-59593-018-3	3	INSPEC	6.666666667
20	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Improving Quantum Circuit Dependability with Reconfigurable Quantum Gate Arrays", Proceedings	3	INSPEC	6.666666667

	2nd ACM International Conference on Computing Frontiers (CF'05), Ischia, Italy, 4-6 May, 2005, pp. (133-144), ISBN: 1-59593-018-3			
21	Mihai Udrescu, Lucian Prodan, Mircea Vladutiu, "Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation", Proceedings 1st ACM Conference on Computing Frontiers, Ischia, Italy, 14-16 Apr, 2004, pp. (96-110), ISBN 1-58113-741-9	3	INSPEC	6.666666667
			TOTAL	115.2222222

A 2.4 Granturi/proiecte de cercetare câștigate prin competiție

A 2.4.1 Director/responsabil partener

A 2.4.1.2 Naționale

Nr. crt.	Grantul/proiectul	Nr. ani	Punctaj	
1	Mihai Udrescu (Director), Mircea Vlăduțiu, Oana Boncalo, Alexandru Amaricăi, Virgil Petcu, Cristian Ruican, Nicolae Velciov. "Proiectarea Circuitelor Cuantice și Reversibile Tolerante la Defectare", R&D Grant CNCSIS, Type A, 380/2007 (Total value: 159000 ROL)	2	20	
2	Mihai Udrescu (Director), Alexandru Topîrceanu, Alexandru Iovanovici, Andrei Lihu, Constantina Gavriliu, Ștefan Mihaicuță, Rodica Dan, Daniela Reisz, Carmen Ardelean, "Internet of Things meets Complex Networks or early prediction and management of Chronic Obstructive Pulmonary Disease", R&D Grant PNCDIII, P2, 31PED/2017 (Total value: 514479 ROL)	2	20	
			TOTAL	40

A 2.4.2 Membru în echipă

A 2.4.2.1 Internaționale

Nr. crt.	Grantul/Proiectul	Nr. ani	Punctaj
1	Valentin Mureșan (Director), Mihai V. Micea (UPT Partner project manager), Alexandru Amaricăi-Boncalo, Oana Amaricăi-Boncalo, Gheorghe Guran, Andrei Tănase, Valentin Stângaciu, Stângaciu Cristina, Valuch Camelia, Tite Teodor, Ivascu Ancuța-Maria, Velciov Ivan, Vig Adelina, Dura Alexandru, Diaconescu Demis, Cosma Marius, Crețu Vladimir, Vlăduțiu Mircea, Ciocârlie Horia, Todincă Doru, Prodan Lucian, Mihai Udrescu, "FALX DACIAE: Unelte și Procese de Dezvoltare SW pentru Aplicații Multimedia Avansate pe Arhitecturi Multi-Core pentru Telefoane Mobile" ("FALX DACIAE: SW Development Tools and Processes for Advanced Multimedia Applications on Mobile Phone Multi-Core Architectures"), POSCCE-A2-O2.1.1 R&D Grant, POSCCE-499-11844/2010 - 2012, EU - Structural Instruments, ANCS - The Romanian National Authority for Scientific Research. Contract 133/04.06.2010. Total value: 2126766 RON (~ 506400 EUR). Value, UPT Partner: 1143416 RON (~ 272300 EUR).	2	8

2	Mircea Vlăduțiu (Director), Antonius Stanciu, Lucian Prodan, <u>Mihai Udrescu</u> , et al., BM-CNFIS 55, European Union: "Promovarea si managementul institutionalizat al activitatilor de educatie permanenta" (Total value: 155105.33 USD)	1	4
3	Stefan Mihaicuta (Director), <u>Mihai Udrescu</u> , Alexandru Topirceanu, Alexandru Iovanovici, et al., Linde RealFund R&D Grant: "Morpheus: A Screening and Monitoring System for Sleep Apnea Syndrome" (Total value: 69865 EUR)	2	8
		TOTAL	20

A 2.4.2.2 Naționale

Nr. crt.	Grantul/proiectul	Nr. ani	Punctaj
1	Mircea Vladutiu (Director), <u>Mihai Udrescu</u> , Lucian Prodan, Oana Boncalo, Alexandru Amăricăi, "Arhitecturi Bioinspirate de Calcul pentru Circuite Logice Reversibile si Cuantice", R&D Grant, PNII IDEI 17/2007 (Total value: ~ 440000 ROL)	3	6
2	Mircea Vladutiu (Director), <u>Mihai Udrescu</u> , Lucian Prodan, Oana Boncalo, Alexandru Amaricai, Versavia Ancusa, Nicolae Velciov, Alin Anton, "Proiectarea Bioinspirata a Aplicatiilor pe Platforme Reconfigurabile", R&D Grant, CNCSIS Type A 643/2005 (Total value: ~ 67800 ROL)	3	6
		TOTAL	12
		TOTAL A2	618.39

A3 Recunoașterea și impactul activității

A 3.1 Citări în cărți, reviste și volume ale unor manifestări științifice

A 3.1.1 Cărți, ISI

Nr.	Sursa citare	Quarter	Lucrarea citată	Nr. autori lucrare citată	Punctaj
1	Lahoz-Beltra, R. Quantum Genetic Algorithms for Computer Scientists. Computers (MDPI) 5, 24; doi:10.3390/computers5040024, 2016		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Implementing Quantum Genetic Algorithms: A Solution Based on Grover's Algorithm. 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, May 2 - 5 2006, pp. 71-82, ACM Press, ISBN 1-59593-302-6	3	2.66667
2	McGlashan, J; Johnstone, M; Creighton, D; de la Haye, K; Allender, S. Quantifying a Systems Map: Network Analysis of a Childhood Obesity Causal Loop Diagram. PLoS ONE 11(10):	Q1	Alexandru Topirceanu, Gabriel Barina, <u>Mihai Udrescu</u> , "MuSeNet: Collaboration in the music artists industry", 1st European Network Intelligence Conference, ENIC,	3	5.33333

	e0165459. doi:10.1371/journal.pone.0165459, 2016		Wroclaw, Poland, 29-30 Sep, 2014, pp. (89-94), ISBN: 978-147996914-2		
3	Emary, E; Zawbaa, HM; Hassaniien, AE. Binary ant lion approaches for feature selection. Neurocomputing 213: 54-65 ISSN: 0925-2312 (doi:10.1016/j.neucom.2016.03.101), 2016	Q2	Cristian Cosariu, Alexandru Iovanovici, Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Bio-inspired redistribution of urban traffic flow using a social network approach", IEEE Congress on Evolutionary Computation, Sendai, Japan, 25-28 May, 2015, pp. 77-84, ISBN 978-1-4799-7492-4	5	3.2
4	Gutierrez, MD; Tenentes, V; Kazmierski, TJ. Susceptible Workload driven Selective Fault Tolerance using a Probabilistic Fault Model. 22nd IEEE International Symposium on On-Line Testing and Robust System Design (IOLTS), pp. 115-120, Catalunya, Spain, Jul 04-06, 2016		Liviu Agnola, Mircea Vladutiu, <u>Mihai Udrescu</u> , Lucian Prodan, "Simplified selective fault tolerance technique for protection of selected inputs via triple modular redundancy", 7th IEEE International Symposium on Applied Computational Intelligence and Informatics (SACI), Timisoara, Romania, 24-26 May, 2012, pp. (267-272), ISBN: 978-1-4673-1013-0	4	2
5	Ahn, CV; Yoo, J.-C. A quantum evolutionary approach to multimodal optimization. Journal of Computational and Theoretical Nanoscience, 13(1): pp. 636-642, 2016		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Implementing Quantum Genetic Algorithms: A Solution Based on Grover's Algorithm. 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, May 2 - 5 2006, pp. 71-82, ACM Press, ISBN 1-59593-302-6	3	2.66667
6	Satsangi, S; Patvardhan, C. Application of Genetic Algorithm for Evolution of Quantum Fourier Transform Circuits. Proc. 2nd Intl. Conf on Computer and Communication Technologies, IC3T 2015, VOL 1 Book Series: Advances in Intelligent Systems and Computing Vol 379, pp: 773-782, 2016		Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Automatic Synthesis for Quantum Circuits using Genetic Algorithms. Proceedings ICANN'07 "Adaptive and Natural Computing Algorithms", LNCS 4431 (Springer-Verlag Berlin Heidelberg), Warsaw, Poland, 2007, pp. 174-183, ISBN: 978-3-540-71589-4	4	2
7	Velasco, A; Montrucchio, B; Rebaudengo, M. KITO tool: A fault injection environment in Linux kernel data structures.		Oana Boncalo, Alexandru Amaricai, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Quantum Circuit's Reliability Assessment with VHDL-Based	4	2

	Microelectronics Reliability 60: pp. 153-162, 2016		Simulated Fault Injection", Microelectronics Reliability, 50(2), pp. (304-311), 2010, Elsevier, ISSN 0026-2714		
8	Houshmand, M; Zamani, MS; Sedighi, M; Houshmand, M. GA-based approach to find the stabilizers of a given sub-space. Genetic Programming and Evolvable Machines. 16(1): pp. 57-71, 2015		Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Automatic Synthesis for Quantum Circuits using Genetic Algorithms. Proceedings ICANNGA'07 "Adaptive and Natural Computing Algorithms", LNCS 4431 (Springer-Verlag Berlin Heidelberg), Warsaw, Poland, 2007, pp. 174-183, ISBN: 978-3-540-71589-4	4	2
9	Hector Garcia, Igor Markov. Simulation of Quantum Circuits via Stabilizer Frames. IEEE Transactions on Computers. 64(8), Pages: 2323-2336, 2015, ISSN: 0018-9340	Q1	Oana Boncalo, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, Alexandru Amaricai, "Using Simulated Fault Injection for Fault Tolerance Assessment of Quantum Circuits", Proceedings IEEE 40th Annual Simulation Symposium, Norfolk VA, USA, 26-28 Mar, 2007, pp. (213-220), ISSN: 1080-241X, ISBN: 0-7695-2814-7	5	3.2
10	Rommel M. Anacan. Optimization of an Arithmetic Logic Unit using Generic Floating Point Algorithm for 12-Bit Architecture. 5th IEEE International Conference on Control System, Computing and Engineering (ICCSCE), Batu Ferringhi, Malaysia, pp. 24-29, 2015		Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo, "Design of Addition and Multiplication Units for High Performance Interval Arithmetic Processor", IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems, Krakow, Poland, 11-13 Apr, 2007, pp. (223-226), ISBN: 1-4244-1161-0	5	1.6
11	SaiToh, Akira; Rahimi, Robabeh; Nakahara, Mikio. A quantum genetic algorithm with quantum crossover and mutation operations. QUANTUM INFORMATION PROCESSING. Volume: 13 Issue: 3 Pages: 737-755, 2014, ISSN: 1570-0755		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Implementing Quantum Genetic Algorithms: A Solution Based on Grover's Algorithm. 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, May 2 - 5 2006, pp. 71-82, ACM Press, ISBN 1-59593-302-6	3	2.66667

12	SaiToh, Akira; Rahimi, Robabeh; Nakahara, Mikio. A quantum genetic algorithm with quantum crossover and mutation operations. QUANTUM INFORMATION PROCESSING. Volume: 13 Issue: 3 Pages: 737-755, 2014, ISSN: 1570-0755		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Grover's Algorithm and the Evolutionary Approach of Quantum Computation. ACSA Report Published: 2004, Publisher: Politehnica University of Timisoara, URL: http://www.acsa.upt.ro/publications/index.htm	3	2.66667
13	Bruguera, JD. Optimizing the representation of intervals. SCIENCE OF COMPUTER PROGRAMMING, Volume: 90 Pages: 21-33 Part: A, 2014, ISSN: 0167-6423		Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo, "Floating Point Multiplication Rounding Schemes for Interval Arithmetic", Proceedings 19th IEEE Conference on Application Specific Systems, Architectures and Processors (ASAP), Leuven, Belgium, 2-4 Jul, 2008, pp. (19-24), ISBN: 978-1-4244-1898-5	5	1.6
14	Ai, Xinbo. Inferring a Drive-Response Network from Time Series of Topological Measures in Complex Networks with Transfer Entropy. ENTROPY, Volume: 16 Issue: 11 Pages: 5753-5776, 2014		Alexandru Topirceanu, <u>Mihai Udrescu</u> , Mircea Vladutiu, "Network Fidelity: A Metric to Quantify the Similarity and Realism of Complex Networks", 3rd IEEE International Conference on Cloud and Green Computing (CGC), Karlsruhe, Germany, Sep 30-Oct 02, 2013, pp. (289-296), ISBN 978-0-7695-5114-2	3	2.66667
15	Elhoushi, Mostafa; El-Kharashi, M. Watheq; Elrefaei, Hatem; Model of a hybrid processor executing C plus plus with additional quantum functions, MICROPROCESSORS AND MICROSYSTEMS Volume: 38 Issue: 8 Pages: 1000-1011 Part: B, 2014		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation", Proceedings 1st ACM Conference on Computing Frontiers, Ischia, Italy, 14-16 Apr, 2004, pp. (96-110), ISBN 1-58113-741-9	3	2.66667
16	Elhoushi, Mostafa; El-Kharashi, M. Watheq; Elrefaei, Hatem; Model of a hybrid processor executing C plus plus with additional quantum functions, MICROPROCESSORS AND MICROSYSTEMS Volume: 38 Issue: 8 Pages: 1000-1011 Part: B, 2014		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "The Bubble Bit Technique as Improvement of HDL-Based Quantum Circuits Simulation", Proceedings IEEE 38th Annual Simulation Symposium, San	3	2.66667

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17	Elhoushi, Mostafa; El-Kharashi, M. Watheq; Elrefaei, Hatem; Model of a hybrid processor executing C plus plus with additional quantum functions, MICROPROCESSORS AND MICROSYSTEMS Volume: 38 Issue: 8 Pages: 1000-1011 Part: B, 2014		<u>Mihai Udrescu</u> . Quantum Circuits Engineering: Efficient Simulation and Reconfigurable Quantum Hardware. Ph.D. Thesis, Universitatea "Politehnica" din Timisoara (UPT), November 25, 2005	1	8
18	Hadjam, FZ; Moraga, C. RIMEP2: Evolutionary Design of Reversible Digital Circuits. ACM Journal on Emerging Technologies in Computing Systems. 11(3):Article Number 27, 2014, ISSN:1550-4832		Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Automatic Synthesis for Quantum Circuits using Genetic Algorithms. Proceedings ICANNGA'07 "Adaptive and Natural Computing Algorithms", LNCS 4431 (Springer-Verlag Berlin Heidelberg), Warsaw, Poland, 2007, pp. 174–183, ISBN: 978-3-540-71589-4	4	2
19	Hector J. Garcia, Igor L. Markov. Quipu: High-performance Simulation of Quantum Circuits using Stabilizer Frames. In Proceedings of the 31st IEEE International Conference on Computer Design (ICCD 2013), Asheville NC, USA, 6-9 Oct. 2013		Oana Boncalo, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vlăduțiu, Alexandru Amăricăi. Using Simulated Fault Injection for Fault Tolerance Assessment of Quantum Circuits, Proceedings IEEE 40th Annual Simulation Symposium, Norfolk VA, USA, 26-28 Mar 2007, pp. 213-220, ISBN: 0-7695-2814-7	4	2
20	Jose MA, Piqueira JRC, Lopes RD. Introduction to quantum programming. Revista Brasileira de Endino de Fisica Volume: 35 Issue: 1 Article Number: 1306 Published: JAN-MAR 2013		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	2.66667
21	Lu, XW (Lu, Xiangwen); Yuan, JB (Yuan, Jiabin); Zhang, WW (Zhang, Weiwei). Workflow of the Grover algorithm simulation incorporating CUDA and GPGPU. COMPUTER PHYSICS COMMUNICATIONS Volume: 184 Issue: 9 Pages: 2035-2041, SEP 2013	Q2	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy,	3	5.33333

			April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9		
22	Banerjee, Anindita; Pathak, Anirban. An Algorithm for Minimization of Quantum Cost. APPLIED MATHEMATICS & INFORMATION SCIENCES Volume: 6 Issue: 1 Pages: 157-165, JAN 2012, ISSN 1935-0090		Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Automatic Synthesis for Quantum Circuits using Genetic Algorithms. Proceedings ICANNGA'07 "Adaptive and Natural Computing Algorithms", LNCS 4431 (Springer-Verlag Berlin Heidelberg), Warsaw, Poland, 2007, pp. 174–183, ISBN: 978-3-540-71589-4	4	2
23	Elhoushi, Mostafa; El-Kharashi, M. Watheq; Elrefaei, Hatem. Modeling a Quantum Processor using the QRAM Model. IEEE Pacific Rim Conference on Communications, Computers and Signal Processing (PACRIM), Victoria, CANADA, Pages: 409-415, AUG 23-26, 2011, ISBN 978-1-4577-0252-5		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	2.66667
24	Metodi, Tzvetan; Faruque, Arvin I.; Chong, Frederic T. Quantum Computing for Computer Architects 2nd Ed. Morgan & Claypool Publishers, 2011, ISBN: 978-1608456192		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	2.66667
25	Metodi, Tzvetan; Faruque, Arvin I.; Chong, Frederic T. Quantum Computing for Computer Architects 2nd Ed. Morgan & Claypool Publishers, 2011, ISBN: 978-1608456192		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Improving Quantum Circuit Dependability with Reconfigurable Quantum Gate Arrays. 2nd ACM International Conference on Computing Frontiers (CF'05), Ischia, Italy, May 4 - 6 2005, pp. 133-144, ACM Press ISBN 1-59593-018-3	3	2.66667
26	Zhang, Gexiang. Quantum-inspired evolutionary algorithms: a survey and empirical study. JOURNAL OF HEURISTICS Volume: 17 Issue: 3 Pages: 303-351, JUN 2011, ISSN: 1381-1231		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Implementing Quantum Genetic Algorithms: A Solution Based on Grover's Algorithm. 3rd ACM International Conference on Computing Frontiers	3	2.66667

			(CF'06), Ischia, Italy, May 2 - 5 2006, pp. 71-82, ACM Press, ISBN 1-59593-302-6		
27	Gutierrez, Eladio; Romero, Sergio; Trenas, Maria A.; Zapata, Emilio L. Quantum computer simulation using the CUDA programming model. COMPUTER PHYSICS COMMUNICATIONS Volume: 181 Issue: 2 Pages: 283-300, FEB 2010, ISSN: 0010-4655	Q2	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	5.33333
28	Saeedi, Mehdi; Sedighi, Mehdi; Zamani, Morteza Saheb. A library-based synthesis methodology for reversible logic. MICROELECTRONICS JOURNAL Volume: 41 Issue: 4 Pages: 185-194, APR 2010, ISSN: 0026-2692		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	2.66667
29	Markowska-Kaczmar, Urszula; Krygowski, Filip. The Influence of Using Design Patterns on the Process of Implementing Genetic Algorithms. 23rd International Conference on Industrial, Engineering and Other Applications of Applied Intelligent Systems, Cordoba, SPAIN, JUN 01-04, 2010, Lecture Notes in Artificial Intelligence Volume: 6097 Pages: 173-182, 2010, ISBN 978-3-642-13024-3		Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. A Genetic Algorithm Framework Applied to Quantum Circuit Synthesis. In N. Krasnogor, G. Nicosia, M. Pavone, D. Pelta (Eds.), Studies in Computational Intelligence (Springer), vol. 129, Proc. International Workshop on Nature Inspired Cooperative Strategies for Optimization (NICSO'07), Acireale, Italy, November 8-10 2007, pp. 419-429, ISBN: 978-3-540-78986-4	4	2
30	Hamid, Lamiaa S. A. ; Shehata, Khaled A. ; El-Ghitani, Hassan ; et al. Design of Generic Floating Point Multiplier and Adder/Subtractor Units. 12TH INTERNATIONAL CONFERENCE ON COMPUTER MODELLING AND SIMULATION (UKSIM) Pages: 615-618, Cambridge, ENGLAND Date: MAR		Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo. Design of Addition and Multiplication Units for High Performance Interval Arithmetic Processor. Proceedings IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems, Krakow, Poland, April	5	1.6

	24-26, 2010, ISBN 978-1-4244-6614-6		11-13 2007, pp. 223-226, ISBN: 1-4244-1161-0		
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32	Gepp, Adrian; Stocks, Phil. A review of procedures to evolve quantum algorithms. GENETIC PROGRAMMING AND EVOLVABLE MACHINES Volume: 10 Issue: 2 Pages: 181-228, JUN 2009, ISSN: 1389-2576		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Implementing Quantum Genetic Algorithms: A Solution Based on Grover's Algorithm. 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, May 2 - 5 2006, pp. 71-82, ACM Press, ISBN 1-59593-302-6	3	2.66667
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35	Gutierrez, Eladio; Romero, Sergio; Trenas, Maria A.; Zapata, Emilio L. Parallel quantum computer simulation on the CUDA architecture. 8th International Conference on Computational Science, Cracow, POLAND, JUN 23-		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy,	3	2.66667

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36	Sofge, Donald A. A Survey of Quantum Programming Languages: History, Methods, and Tools. 2nd International Conference on Quantum, Nano and Micro Technologies. ICQNM 2008, St Luce, MARTINIQUE, FEB 10-15, 2008, Pages: 66-71, ISBN: 978-1-424-44228-7		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	2.66667
37	Sofge, Donald A. Quantum Programming: Past, Present, Future. International Conference on Foundations of Probability and Physics-5. Vaxjo, SWEDEN, AUG 24-27 2008, Pages: 178-185 Published: 2009, ISBN 978-0-7354-0636-0		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	2.66667
38	Stepney, Susan; Clark, John A. Searching for quantum programs and quantum protocols. JOURNAL OF COMPUTATIONAL AND THEORETICAL NANOSCIENCE Volume: 5 Issue: 5 Pages: 942-969, MAY 2008, ISSN: 1546-1955		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Implementing Quantum Genetic Algorithms: A Solution Based on Grover's Algorithm. 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, May 2 - 5 2006, pp. 71-82, ACM Press, ISBN 1-59593-302-6	3	2.66667
39	Akkas, Ahmet. Dual-mode floating-point adder architectures. JOURNAL OF SYSTEMS ARCHITECTURE Volume: 54 Issue: 12 Pages: 1129-1142, NOV 2008, ISSN: 1383-7621	Q2	Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo. Design of Addition and Multiplication Units for High Performance Interval Arithmetic Processor. Proceedings IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems, Krakow, Poland, April 11-13 2007, pp. 223-226, ISBN: 1-4244-1161-0	5	3.2
40	Gutierrez, Eladio ; Romero, Sergio ; Trenas, Maria A. ; Zapata, Emilio L. Simulation of quantum gates on a		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A	3	2.66667

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41	Wang, Ping ; Zeng, Sanyou ; Yan, Jingfeng ; et al. Using multi-objective evolutionary algorithm in PLD model to implement the automatic design of logic circuits. 2nd International Symposium on Intelligence Computation and Application (ISICA 2007), Wuhan, CHINA, SEP 21-23, 2007, Pages: 120-123, 2007, ISBN: 978-3540745808		Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu. Self-Repairing Embryonic Memory Arrays. IEEE NASA/DoD Conference on Evolvable Hardware (EH'04), Seattle WA, USA, June 24 - 26, 2004, pp. 130-137, ISBN 0-7695-2145-2	3	2.66667
42	Zhai Zhang, Youren Wang, Shanshan Yang, Rui Yao, Jiang Cui. The research of self-repairing digital circuit based on embryonic cellular array. Journal of Neural Computing & Applications, Springer London, February 2007, ISSN 0941-0643		Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu. Self-Repairing Embryonic Memory Arrays. IEEE NASA/DoD Conference on Evolvable Hardware (EH'04), Seattle WA, USA, June 24 - 26, 2004, pp. 130-137, ISBN 0-7695-2145-2	3	2.66667
43	Bhave Aasavari, Montagne Euripides, Granados Edgar. Describing quantum circuits with systolic arrays. 17th IEEE International Conference on Application-Specific Systems, Architectures and Processors, Steamboat Springs, SEP 11-13, 2006, Pages: 109-113, 2006, ISBN: 978-0769526829		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	2.66667
44	Gay, Simon J. Quantum programming languages: survey and bibliography. MATHEMATICAL STRUCTURES IN COMPUTER SCIENCE Volume: 16 Issue: 4 Pages: 581-600, AUG 2006, ISSN: 0960-1295		<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy,	3	2.66667

			April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9		
45	Spector, Lee. Automatic Quantum Computer Programming: A Genetic Programming Approach. Springer (October 4, 2006) ISBN: 978-0387364964		<u>Mihai Udrescu</u> . Quantum Circuits Engineering: Efficient Simulation and Reconfigurable Quantum Hardware. Ph.D. Thesis, Universitatea "Politehnica" din Timisoara (UPT), November 25, 2005	1	8
				TOTAL	137.067
A 3.1.2 BDI					
Nr. crt.	Sursa citare	BDI	<u>Lucrarea citată</u>	Nr. autori lucrare citată	Punctaj
1	Finaev, V; Beloglazov, D; Shapovalov, I; Kosenko, E; Kobersy, I. Evolutionary algorithm for intelligent hybrid system training. ARPN Journal of Engineering and Applied Sciences 10(6):2386-2391, 2016, ISSN:1819-6608	Scopus	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu, "Adaptive vs. Self-Adaptive Parameters for Evolving Quantum Circuits", 9th International Conference on Evolvable Systems: From Biology to Hardware (ICES 2010), York, UK, Lecture Notes in Computer Science (Springer), vol. 6274, 6-8 Sep, 2010, pp. (348-359), ISSN: 0302-9743	4	1
2	Wang, Z; Zheng, M; Tu, J; Wang, J; Li, L. Observability-oriented selective triple modular redundancy for SRAM FPGA. Journal of Computer-Aided Design and Computer Graphics, 27(11):2184-2191, 2015, ISSN:1003-9775	Scopus	Liviu Agnola, Mircea Vladutiu, Mihai Udrescu, Lucian Prodan, "Simplified selective fault tolerance technique for protection of selected inputs via triple modular redundancy", 7th IEEE International Symposium on Applied Computational Intelligence and Informatics (SACI), Timisoara, Romania, 24-26 May, 2012, pp. (267-272), ISBN: 978-1-4673-1013-0.	4	1
3	Daoud, L; Zydek, D; Selvaraj, H. A Survey on Design and Implementation of Floating Point Adder in FPGA. Advances in Intelligent Systems and Computing 1089: 885-892, 2015, ISSN:2194-5357	Scopus	Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, Mihai Udrescu, Oana Boncalo, "Exploiting Parallelism in Double Path Adders' Structure for Increased Throughput of Floating Point Addition", Proceedings 10th EUROMICRO Conference on Digital System Design, Architectures, Methods and	5	0.8

			Tools (DSD 2007), Lübeck, Germany, 29-31 Aug, 2007, pp. (132-137), ISBN: 0-7695-2978-X.		
4	Wang, X; Jiao, L; Li, Y; Qi, Y; Wu, J. A variable-length chromosome evolutionary algorithm for reversible circuit synthesis. Journal of Multiple-Valued Logic and Soft Computing 25(6):643-671, 2015, ISSN:1542-3980	Scopus	Cristian Ruican, Mihai Udrescu, Lucian Prodan, Mircea Vladutiu, "Genetic Algorithm Based Quantum Circuit Synthesis with Adaptive Parameters Control", IEEE Congress on Evolutionary Computation (CEC 2009), Trondheim, Norway, 18-21 May 2009, pp. (896-903), ISBN 978-1-4244-2959-2.	4	1
5	Nabizadeh, Z.; Sedighi, M.; Zamani, M.S. Simultaneous improvement of area, delay, and fault tolerance in quantum circuits. Proceedings - 17th CSI International Symposium on Computer Architecture and Digital Systems, CADSD 2013, ISBN: 978-147990562-1	Scopus	Mihai Udrescu, Lucian Prodan, Mircea Vladutiu. Improving Quantum Circuit Dependability with Reconfigurable Quantum Gate Arrays. 2nd ACM International Conference on Computing Frontiers (CF'05), Ischia, Italy, May 4 - 6 2005, pp. 133-144, ACM Press ISBN 1-59593-018-3	3	1.333333
6	Bojić, A. Quantum Algorithm for Finding a Maximum Clique in an Undirected Graph. Journal of Information and Organizational Sciences. Vol 36, No 2, 2012, Pages 91-98, ISSN 1846-3312	Scopus	Mihai Udrescu, Lucian Prodan, Mircea Vladutiu. Improving Quantum Circuit Dependability with Reconfigurable Quantum Gate Arrays. 2nd ACM International Conference on Computing Frontiers (CF'05), Ischia, Italy, May 4 - 6 2005, pp. 133-144, ACM Press ISBN 1-59593-018-3	3	1.333333
7	Hattangady, S., Edmonson, W., Alexander, W. Block Floating point interval ALU for digital signal processing. Reliable Computing, Volume 15, Issue 2, June 2011, Pages 69-80, ISSN: 13853139	Scopus	Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, <u>Mihai Udrescu</u> , Oana Boncalo. Design of Addition and Multiplication Units for High Performance Interval Arithmetic Processor. Proceedings IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems, Krakow, Poland, April 11-13 2007, pp. 223-226, ISBN: 1-4244-1161-0	5	0.8
8	Wang, Y.-R. , Huang, Y.-Y. , Feng, R., Zhang, Z. Evolutionary design technology of quantum reversible logic circuit based on matrix	Scopus	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Genetic Algorithm Based Quantum Circuit Synthesis with Adaptive	4	1

	coding. Acta Electronica Sinica, Volume 39, Issue 11, November 2011, Pages 2576-2582, ISSN: 03722112		Parameters Control. IEEE Congress on Evolutionary Computation (CEC 2009), Trondheim, Norway, May 18-21 2009, pp. 896-903, ISBN 978-1-4244-2959-2		
9	Maity, S., Mandal, S.B., Pal, A., Chakrabarti, A., Roy, T. Design of an efficient quantum circuit simulator. Proceedings - 2010 International Symposium on Electronic System Design, ISED 2010, Article number 5715148, Pages 50-55, ISBN: 978-076954294-2	Scopus	Cristian Ruican, <u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Genetic Algorithm Based Quantum Circuit Synthesis with Adaptive Parameters Control. IEEE Congress on Evolutionary Computation (CEC 2009), Trondheim, Norway, May 18-21 2009, pp. 896-903, ISBN 978-1-4244-2959-2	4	1
10	Hözl, M., Rauschmayer, A., Wirsing, M. Engineering of software-intensive systems: State of the art and research challenges. Lecture Notes in Computer Science (including subseries Lecture Notes in Artificial Intelligence and Lecture Notes in Bioinformatics) Volume 5380 LNCS, 2008, Pages 1-44, ISSN: 03029743 ISBN: 3540894365;978-354089436-0	Scopus	Lucian Prodan, <u>Mihai Udrescu</u> , O. Boncalo, M. Vladutiu. Design for Dependability in Emerging Technologies. ACM Journal of Emerging Technologies in Computing, Volume 3, Issue 2 (Article 6), July 2007, ISSN 1550-4832	4	1
11	Zhang, Y.; Wang, Y.; Yang, S.; Xie, M. Design of a cell in embryonic systems with improved efficiency and fault-tolerance. Lecture Notes in Computer Science (including subseries Lecture Notes in Artificial Intelligence and Lecture Notes in Bioinformatics) 4684 LNCS, pp. 129-139, 2007, ISBN: 978-354074625-6	Scopus	Lucian Prodan, Mihai Udrescu, Mircea Vladutiu. Self-Repairing Embryonic Memory Arrays. IEEE NASA/DoD Conference on Evolvable Hardware (EH'04), Seattle WA, USA, June 24 - 26, 2004, pp. 130-137, ISBN 0-7695-2145-2	3	1.333333
12	Stauffer, A., Mange, D., Tempesti, G. Bio-inspired computing machines with self-repair mechanisms. Lecture Notes in Computer Science (including subseries Lecture Notes in Artificial Intelligence and Lecture Notes in Bioinformatics) Volume 3853 LNCS, 2006, Pages 128-140, 2nd International Workshop on	Scopus	Lucian Prodan, <u>Mihai Udrescu</u> , Mircea Vladutiu. Self-Repairing Embryonic Memory Arrays. IEEE NASA/DoD Conference on Evolvable Hardware (EH'04), Seattle WA, USA, June 24 - 26, 2004, pp. 130-137, ISBN 0-7695-2145-2	3	1.333333

	Biologically Inspired Approaches to Advanced Information Technology, BioADIT 2006, Osaka, 26-27 January 2006, ISSN: 03029743 ISBN: 3540312536;978-354031253-6				
13	Metodi, T.S., Chong, F.T. Quantum computing for computer architects. Synthesis Lectures on Computer Architecture, Volume 1, 1 January 2006, Pages 1-152, ISSN: 19353235	Scopus	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. 1st ACM / SIGMICRO Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ACM Press, ISBN 1-58113-741-9	3	1.333333
14	Metodi, T.S., Chong, F.T. Quantum computing for computer architects. Synthesis Lectures on Computer Architecture, Volume 1, 1 January 2006, Pages 1-152, ISSN: 19353235	Scopus	<u>Mihai Udrescu</u> , Lucian Prodan, Mircea Vladutiu. Improving Quantum Circuit Dependability with Reconfigurable Quantum Gate Arrays. 2nd ACM International Conference on Computing Frontiers (CF'05), Ischia, Italy, May 4 - 6 2005, pp. 133-144, ACM Press ISBN 1-59593-018-3	3	1.333333
				TOTAL	15.6

A 3.2 Membru în comitetele de redacție sau comiteele științifice ale revistelor indexate ISI, chair, co-chair sau membru în comitetele de organizare ale manifestărilor științifice internaționale indexate ISI

Nr. crt.	Activitatea	Punctaj	
1	<u>Membru comitet tehnic de program</u> , International Conference on Hardware/Software Codesign and System Synthesis (CODES+ISSS), 2013-2014.	10	
2	<u>Membru comitet tehnic de program</u> , IEEE Congress on Evolutionary Computation, 2009.	10	
3	<u>Membru comitet tehnic de program</u> , IEEE International Conference on Computer and Information Technology, 2007-2009.	10	
4	<u>Membru comitet tehnic de program și session chair</u> , IEEE International Symposium on Design & Diagnostics of Electronic Circuits & Systems, 2008-2017.	10	
		TOTAL	40

A 3.3 Membru în comitetele de redacție sau comiteele științifice ale revistelor indexate ISI, chair, co-chair sau membru în comitetele de organizare ale manifestărilor științifice internaționale indexate BDI

Nr. crt.	Activitatea	Punctaj	
1	<u>Membru comitet de organizare și Copresedinte sesiune specială "Dependability Issues in Emerging Technologies"</u> , 3rd ACM Computing Frontiers Conference 2006.	6	
		TOTAL	6

A 3.4 Premii în domeniu conferite de Academia Română, ASTR, AOSR, sau premii internaționale de prestigiu		
Nr. crt.	Premiul decernat	Punctaj
1	Best Paper Award for Alexandru Topirceanu and Mihai Udrescu, "FMNet: Physical Trait Patterns in the Fashion World" awarded at the 2nd European Network Intelligence Conference, ENIC, Karlskrona, Sweden, 21-22 Sep, 2015	15
		TOTAL
		15
		TOTAL A3
		210.73