

Cod DFI	CodRSI	CodDL	CodS
20	60	10	10

ciclul	c1c2c3	a1a2
L	002	17

CURRICULA  
Academic year 2017 - 2018

	3 <sup>th</sup> YEAR												4 <sup>th</sup> YEAR																																																											
	SEMESTER 5						SEMESTER 6						SEMESTER 7						SEMESTER 8																																																					
1	Computer networks						Artificial intelligence fundamentals						Elective 7 (is selected from positions 1-4 of list)						Elective 13 (is selected from positions 1-4 of list)																																																					
	L002.17.05.D1	4	E	28	0	28	0	DD	38	L002.17.06.D1	4	E	28	0	28	0	DD	38	L002.17.07.SX-ij	5	E	28	0	28	0	DS	64	L002.17.08.SX-ij	4	E	28	0	14	0	DS	52																																				
2	Computer engineering fundamentals						Embedded systems						Elective 8 (is selected from positions 1-7 of list)						Elective 14 (is selected from positions 1-8 of list)																																																					
	L002.17.05.D2	4	E	28	0	14	14	DD	38	L002.17.06.D2	4	E	28	0	14	14	DD	38	L002.17.07.SY-ij	5	E	28	0	28	0	DS	64	L002.17.08.SY-ij	4	E	28	0	14	0	DS	52																																				
3	Operating systems						Computer graphics						Elective 9 (is selected from positions 1-7 of list)						Elective 15 (is selected from positions 1-8 of list)																																																					
	L002.17.05.D3	4	E	28	0	28	0	DD	38	L002.17.06.D3	4	E	28	0	14	14	DD	42	L002.17.07.SZ-ij	5	E	28	0	28	0	DS	64	L002.17.08.SZ-ij	4	E	28	0	14	0	DS	52																																				
4	Digital microsystems design						Elective 4 (is selected from positions 1 and 2 of list)						Elective 10 (is selected from positions 1-7 of list)						Elective 16 (is selected from positions 1-8 of list)																																																					
	L002.17.05.D4	4	E	28	0	14	14	DD	38	L002.17.06.D4-ij	4	E	28	0	28	0	DD	38	L002.17.07.SU-ij	5	E	28	0	28	0	DS	64	L002.17.08.SU-ij	4	E	28	0	14	0	DS	52																																				
5	Management and marketing						Elective 5 (is selected from positions 3-6 of list)						Elective 11 (is selected from positions 8-10 of list)						Development of diploma thesis <sup>1)</sup>																																																					
	L002.17.05.F5	4	D	28	28	0	0	DF	18	L002.17.06.SX-ij	4	D	28	0	28	0	DS	38	L002.17.07.SV-ij	5	D	28	0	28	0	DS	64	L002.17.08.S5	4	D			196	DS																																						
6	Elective 2 (is selected from positions 1 and 2 of list)						Elective 6 (is selected from positions 3-6 of list)						Elective 12 (is selected from positions 8-10 of list)						Diploma exam <sup>2)</sup>																																																					
	L002.17.05.SX-ij	4	D	28	0	28	0	DS	38	L002.17.06.SY-ij	4	D	28	0	28	0	DS	38	L002.17.07.SW-ij	5	D	28	0	28	0	DS	64	L002.17.08.S6	10	E			0	DS																																						
7	Elective 3 (is selected from positions 1-3 of list)												Diploma project						Optional - choose from other specialization's curricula																																																					
	L002.17.05.SY-ij	4	D	28	0	28	0	DS	38									L002.17.07.S7		0	0	0	28	DS		L002.17.08.f7						f																																								
8	Applied activities (60 hours)						Applied activities (180 hours)																																																																	
	L002.17.05.D8	2	C					DD	60	L002.17.06.D8	6	C					DD	180																																																						
9	Optional - choose from other specialization's curricula						Optional - choose from other specialization's curricula																																																																	
	L002.17.05.f9-ij						f			L002.17.06.f9-ij							f																																																							
total/ sem.	ore:			392			VPI:			306			ore:			336			VPI:			412			ore:			364			VPI:			384			ore:			364			VPI:			208																										
	credite:			30			evaluări:			4E, 3D, 1C			credite:			30			evaluări:			4E, 2D, 1C			credite:			30			evaluări:			4E, 2D			credite:			30			evaluări:			5E, 1D																										
total/ săpt.	ore:			28						ore:			24						ore:			26						ore:			26																																									
	din care:			14			2			10			2			(c, s, l, p)			din care:			12			0			10			2			(c, s, l, p)			din care:			12			0			12			2			(c, s, l, p)			din care:			8			0			4			14			(c, s, l, p)		

1) lasting 7 weeks x 28 hours of which 2 weeks x 28 hours internship

2) consists of: fundamental and speciality knowledge assessment, diploma exam

**Professional competences:**

- C1 - Operating with the scientific, engineering and IT fundamentals
- C2 - Designing hardware, software and communication components
- C3 - Problem solving by using computer science and engineering tools
- C4 - Improving the performance of hardware, software and communication systems
- C5 - Designing, managing the life cycle, integrating and managing the integrity of hardware, software and communication systems
- C6 - Designing intelligent systems

**Transversal competences:**

- CT1 - Behaving honorably, responsibly and ethical, according to the law, to ensure problem solving
- CT2 - Identifying, describing and executing the processes of project management, by fulfilling various roles within the team, and describing the results in the field of activity, in a clear and concise manner, verbal and in writing, using the Romanian language and an international language
- CT3 - Proving action and initiative spirit to get current with the knowledge at professional, economic and management levels

**DISCIPLINE OPTIONALE (ELECTIVE DISCIPLINES)**  
Academic year 2017 - 2018

		3 <sup>th</sup> YEAR										4 <sup>th</sup> YEAR																																
		SEMESTER 5					SEMESTER 6					SEMESTER 7					SEMESTER 8																											
01	Elective 2,3 - Large scale integrated circuits											Elective 4 - Digital signal processing											Elective 7,8,9,10 - Peripheral interfaces and devices											Elective 13,14,15,16 - Reconfigurable computing										
	L002.17.05.S#-01	4	D	28	0	28	0	DS	38	L002.17.06.D4-01	4	E	28	0	28	0	DD	38	L002.17.07.S#-01	5	E	28	0	28	0	DS	64	L002.17.08.S#-01	4	E	28	0	14	0	DS	52								
02	Elective 2,3 - Software systems design											Elective 4 - Compilation techniques											Elective 7,8,9,10 - Digital telecommunication											Elective 13,14,15,16 - Computer systems reliability										
	L002.17.05.S#-02	4	D	28	0	28	0	DS	38	L002.17.06.D4-02	4	E	28	0	28	0	DD	38	L002.17.07.S#-02	5	E	28	0	28	0	DS	64	L002.17.08.S#-02	4	E	28	0	14	0	DS	52								
03	Elective 3 - WEB application design											Elective 5,6 - Microprocessor systems											Elective 7,8,9,10 - Modeling and simulation											Elective 13,14,15,16 - Expert systems										
	L002.17.05.S#-03	4	D	28	0	28	0	DS	38	L002.17.06.S#-03	4	D	28	0	28	0	DS	38	L002.17.07.S#-03	5	E	28	0	28	0	DS	64	L002.17.08.S#-03	4	E	28	0	14	0	DS	52								
04												Elective 5,6 - Computer engineering											Elective 7,8,9,10 - Software verification and validation											Elective 13,14,15,16 - Software project management										
												L002.17.06.S#-04	4	D	28	0	28	0	DS	38	L002.17.07.S#-04	5	E	28	0	28	0	DS	64	L002.17.08.S#-04	4	E	28	0	14	0	DS	52						
05												Elective 5,6 - Complexity and computability											Elective 8,9,10 Mobile systems and applications											Elective 14,15,16 - Data mining										
												L002.17.06.S#-05	4	D	28	0	28	0	DS	38	L002.17.07.S#-05	5	E	28	0	28	0	DS	64	L002.17.08.S#-05	4	E	28	0	14	0	DS	52						
06												Elective 5,6 - Design and architecture of complex software systems											Elective 8,9,10 - Computer aided design											Elective 14,15,16 - Fuzzy logic and applications										
												L002.17.06.S#-06	4	D	28	0	28	0	DS	38	L002.17.07.S#-06	5	E	28	0	28	0	DS	64	L002.17.08.S#-06	4	E	28	0	14	0	DS	52						
07																							Elective 8,9,10 - Database design											Elective 14,15,16 - Distributed Programming										
																							L002.17.07.S#-07	5	E	28	0	28	0	DS	64	L002.17.08.S#-07	4	E	28	0	14	0	DS	52				
08																							Elective 11,12 - Computer security											Elective 14,15,16 - Multimedia systems										
																							L002.17.07.S#-08	5	D	28	0	28	0	DS	64	L002.17.08.S#-08	4	E	28	0	14	0	DS	52				
09																							Elective 11,12 - Concurrent and event-based programming																					
																							L002.17.07.S#-09	5	D	28	0	28	0	DS	64													
10																							Elective 11,12 - Verification and validation of hardware systems																					
																							L002.17.07.S#-10	5	D	28	0	28	0	DS	64													
11																																												
12																																												

**Nota:** Din fiecare dintre grupurile de **Discipline optionale** se activează un număr de discipline în funcție de opțiunile studenților, de numărul studenților și de acoperirea financiară.

RECTOR,  
Prof.univ.dr.ing.Viorel-Aurel ȘERBAN

DECAN,  
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