

PERSONAL INFORMATION



Aurel-Stefan GONTEAN



✉ aurel.gontean@upt.ro;

Sex M | Date of birth | Nationality Romanian

POSITION

WORK EXPERIENCE

2009 - Visiting Professor

Baden-Wuerttemberg Cooperative State University Loerrach, www.ba-loerrach.de, Germany

Courses: Computer Architectures; Microcontrollers; Advanced Electronics

2004 - 2012 Vicedean

Electronics and Telecommunications Faculty, www.etc.upt.ro, 300223 Timisoara, ROMANIA

Responsibilities: IT Infrastructure, funding monitoring, cooperation with economic partners

2008 - 2009 EU expert for evaluating project proposals

Scientific Research Fund, Sofia, Bulgaria,

2008 - PhD Advisor

5 PhD Thesis delivered; 4 under development

Sector Electronics and Telecommunications

2005 - Professor

Politehnica University of Timisoara, www.upt.ro, Electronics and Telecommunications Faculty, www.etc.upt.ro, Applied Electronics Department, 2 V. Parvan Bv., 300199 Timisoara, ROMANIA

Courses Digital Integrated Circuits, Databases, Microcontrollers, Programmable Logic Devices, VB.net

2004 - National expert for evaluating project proposals

National Agency for Scientific Research, ANCS, www.ancs.ro

1994 - 2000 Manager

Saratoga Ltd., Timisoara, ROMANIA

- A 35 employees IT company
- **Main sector:** computer networks

EDUCATION AND TRAINING

2014 EuroTraining (Timisoara, Romania)

- Microelectronics

2009 Agilent Trainings (Holland)

- RF Advanced Measurements

2008 Labview Trainings

- Basics, DAQ, FPGA

- 1998 **PhD Degree**
Politehnica University of Timisoara, ROMANIA
 - FPGA Desing
- 1996 **3 months scholarship** at Central Lancashire University, Preston, England
 - VLSI Design oriented
- 1995 **3 months scholarship** at Fachhochschule Wiesbaden, Germany
 - Programmable Logic Design oriented
- 1993 **3 days FPGA Workshop**, Texas Instruments, Germany
 - FPGA Design
- 1986 **Engineer**
Politehnica University of Timisoara, Electronics and Telecommunications Faculty, Timisoara, ROMANIA

PERSONAL SKILLS

Mother tongue(s) Romanian

Other language(s)

	UNDERSTANDING		SPEAKING		WRITING
	Listening	Reading	Spoken interaction	Spoken production	
English	C2	C2	C2	C1	C1
German	B1	B2	B1	B1	B1
French	B1	B2	A2	A2	B1

Levels: A1/2: Basic user - B1/2: Independent user - C1/2 Proficient user
Common European Framework of Reference for Languages

Managerial skills Manager both in private and public domains, over 12 years of experience

Computer skills

- Matlab, LabVIEW, MS Access, Visual Basic.net, C
- CorelDraw

Design

- CAD (Eagle, Proteus, VHDL)
- Embedded Systems (Hardware, Programming)
- Digital design, FPGA applications and PLC applications

Driving licence B Category

ADDITIONAL INFORMATION

Publications	150+ papers, 7 books, 2 international books (chapters), 25+ contracts and grants, 100+ citations, 2 patents
Projects: 2008 -	Head of VLSI Lab in the Renewable Energies Research Institute, Timisoara, Romania. <ul style="list-style-type: none">The VLSI Lab is 1.5 million Euro funded and the whole Institute is an EU 15 million Euro investment
Reviewer	Energy Conversion and Management - ISI Journal (2013 - present) TSP, PDS, ISETC - ISI conferences (2004 - present) ISSE, SIITME, ISSE - IEEE Conferences (2008 - present)
Honours and awards	Best paper Award IDAACS 2011 IEEE Award, 2014
Memberships	<ul style="list-style-type: none">AFCEA (2013 -)EPETRUN (2009 -)IEEE (1999 -)π - TEAM, Texas Instruments, Europe (1994 -)
References	<ul style="list-style-type: none">Prof. Dr.Ing. Axel Sikora, University of Applied Sciences, Offenburg, Germany, www.hs-offenburg.de, axel.sikora@hs-offenburg.deProf. Dr.Otto Schmidt, DualeHochschule Baden-Württemberg, Loerrach, Germany, www.dhbw-loerrach.de/index.php?id=schmidt, schmidt@dhbw-loerrach.deProf. Dr.Ing. Peter Fromm, University of Applied Sciences, Darmstadt, Germany, www.eit.h-da.de, fromm@eit.h-da.de
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