

## **LISTA LUCRĂRILOR PROPRII LA CARE FACE REFERIRE TEZA DE DOCTORAT**

**Ing. Flavius Opritoiu**

- [1] F. Opritoiu, and M. Vlăduțiu, "Research Concerning Configuration of Cryptochips with Testability Facilities," Diploma Project, 2007.
- [2] F. Opritoiu, and M. Vlăduțiu, "Cryptochip implementations with Built-In Self Test features applied to AES standard," The Claude Shannon Institute Workshop on Coding & Cryptography, 2008.
- [3] F. Opritoiu, M. Vlăduțiu, L. Prodan, and M. Udrescu, "Built-In Self Test Applicability for the Non-Linear Operations of Advanced Encryption Standard," The 5th International Symposium on Applied Computational Intelligence and Informatics, pp. 307-312, May 28–29, 2009.
- [4] F. Opritoiu, M. Vlăduțiu, M. Udrescu, and L. Prodan, "Round-Level Concurrent Error Detection Applied to Advanced Encryption Standard," The 12th IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems, pp. 270-275, April 15-17, 2009.
- [5] F. Opritoiu, M. Vlăduțiu, L. Prodan, and M. Udrescu, "A High-Speed AES Architecture Implementation," in ACM International Conference on Computing Frontiers, 2010, pp. 95-96.
- [6] F. Opritoiu, M. Vlăduțiu, M. Udrescu, and L. Prodan, "Concurrent Error Detection for Multiplicative Inversion of Advanced Encryption Standard," in The 10th IEEE International Conference on Computer and Information Technology, accepted for publication, 2010.