

Procedures and criteria for evaluation and recognition of student's progress within course activities during the semester  
**Electrical Engineering and Telecommunications – Politehnica International Programme ( 4 year bachelor), Specialisation  
 Telecommunications Systems Technology - 3<sup>rd</sup> year, 4<sup>th</sup> year**

**Study Program Missions**

<b>Didactic Mission</b>	Developing researcher skills and enhancing the competence as specialists by improving the students abilities in design, modeling, simulation, testing and implementation of analog and digital circuits, in exploiting signal processing techniques, in using computer languages and specialized programs to design electronic circuits starting from the technical specifications and to study their operations, in different conditions.
<b>Research Mission</b>	Developing abilities in analysing, in extracting technical specifications for a specific circuit design, in developing systems architecture and in modeling electronic circuits using iterative design, computer simulation and testing in real-time. Developing students skills for working in research teams.

Nr. crt.	Year of study	Course name	Evaluation procedures and criteria (written/oral examination, / colocvium, No. of examiners, No. of questions, evaluation criteria, requirements for minimum mark, requirements for maximum mark, evaluation conditions setup)	Quantification/recognition of the student progress/activity during the semester
1	III	Management and Marketing	<ul style="list-style-type: none"> <li>• 2 written evaluation for 2 hours with 2 subjects from theory/courses (50% from mark) and one subject from seminars – a problem solving (50% from mark); every evaluation is 50% from the mark and it is 50% from final mark;</li> <li>• Evaluation criteria for theory: reproducing subjects from course material and discussions in the class;</li> <li>• Evaluation criteria for problem solving: 4 questions with 2/3/3/1 points (1 point from start)</li> <li>• For minimum mark is necessary a minimum mark of 5 for every subject of evaluation</li> </ul>	<ul style="list-style-type: none"> <li>• Student participation at seminars and courses, answering questions, working at table, solving problems and 2 homeworks (one from management part and one from marketing part)</li> <li>• The activity mark is 50% from the final mark</li> </ul>

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2	III	Electronic Instrumentation	<ul style="list-style-type: none"> <li>• Written exam including theory and applications.</li> <li>• Practical tests which are held in a laboratory room and presented in a written report. The average mark of these reports represents 1/3 of the final mark.</li> <li>• Minimum 2 examiners.</li> <li>• Written theoretical part – 2 questions. Written applications part – 5 questions.</li> <li>• The mark 5 means 50% correct answers for both theory and applications,</li> <li>• The mark 10 means 100% correct answers for both theory and applications.</li> <li>• The mark from the written exam including theory and applications represents 2/3 of the final mark. The written exam will be held in a course room.</li> <li>• The final mark will be computed if the marks from the written exam and the practical tests are both equal or greater than 5 and will be rounded according RODPI of UPT.</li> </ul>	<ul style="list-style-type: none"> <li>• The marks equal or greater than 5 for the exam and for the laboratory activity will be recognized unconditionally at any moment.</li> </ul>
3	III	Radio Communications	<ul style="list-style-type: none"> <li>• Distributed written examination in two sessions, with 2 examiners.</li> <li>• 10-12 questions, including 6-7 for theoretical knowledge, and 3-4 problems.</li> <li>• Final mark : <math>\frac{2}{3} * (\frac{1}{3} \text{ theoretical knowledge} + \frac{2}{3} \text{ problems solving}) + \frac{1}{3} \text{ seminar activity}</math></li> <li>• Mark 5 : minimum 5 at theoretical knowledge, minimum 5 at problems solving and minimum 5 at seminar activity</li> <li>• Mark 10 : arithmetical mean greater than 9.5.</li> <li>• Provided by the faculty.</li> </ul>	<ul style="list-style-type: none"> <li>• The grade for the activity during the semester represents the simple average of the laboratory grade. The laboratory grade reflects the practical skills of the student, and the results obtained by short tests.</li> <li>• The grade 5 or higher for the written exam or for the activity during the semester, remains valid for any next examination.</li> </ul>
4	III	Virtual Instrumentation	<ul style="list-style-type: none"> <li>• Written exam and practical tests.</li> <li>• Practical tests which are held in a laboratory room. The average mark of these practical tests represents 25% from the final mark.</li> <li>• One multiple-choice evaluation with 30 questions.</li> <li>• Each question has 4 possible answers, only one correct.</li> <li>• Minimum 2 supervisors.</li> <li>• The mark 5 means 50% correct answers,</li> <li>• The mark 10 means 100% correct answers.</li> <li>• The mark from the multiple choice test and practical test represents 75% from the final mark. The multiple choice test and practical test will be held in a course room.</li> <li>• The final mark will be computed if the marks from the multiple choice test and the practical tests are equal or greater than 5 and will be rounded according RODPI of UPT.</li> </ul>	<ul style="list-style-type: none"> <li>• The marks equal or greater than 5 for the exam and for the laboratory activity will be recognized unconditionally at any moment.</li> </ul>
5	III	Information Theory and Coding	<ul style="list-style-type: none"> <li>• Written exam.</li> <li>• Two internal examiners.</li> <li>• A theoretical subject (20 mcqs) and 2 to 4 problems. Duration: 2,5h. Weight of activity mark in the final mark: 33%.</li> <li>• A passing mark of 5 is obtained for solving half of the subjects, if the activity mark is at least 5.</li> <li>• For the maximum mark (10), 90% of the exam subjects must be properly solved and the</li> </ul>	<ul style="list-style-type: none"> <li>• The marks equal or greater than 5 for the exam and for the laboratory activity will be recognized unconditionally at any moment.</li> </ul>

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			activity mark must be at least 9,5. Room assigned by the executive board of the faculty. Exam subjects multiplied for each student.	
6	III	Data Communications	<ul style="list-style-type: none"> <li>• Written, 2 examiners</li> <li>• 8 questions (4 theoretical questions and 4 exercises)</li> <li>• Requirements for minimum mark: the average of the marks to be at least 5 for the theoretical part and at least 5 for the practical exercises</li> <li>• The list with possible questions is published on the Internet, before the beginning of the exam session. All the subjects are chosen from that list.</li> </ul>	<ul style="list-style-type: none"> <li>• After each module of three laboratory works the students will have to pass a test, from the theoretical background and the practical activities related to the laboratory works</li> <li>• Another test has as subject exercises that represent applications of the theoretical topics discussed during the courses</li> <li>• The arithmetical mean of the three marks will constitute the basis of the student progress evaluation during the semester</li> </ul>
7	III	High Frequency Techniques	<ul style="list-style-type: none"> <li>• Colocvium (two tests)</li> <li>• No. of examiners: 2</li> <li>• No. of questions: 6= 4 theory + 2 problems per test</li> <li>• Requirement for minimum mark: at most two theory questions left untackled</li> <li>• Written examination of 1h and 15 minutes / test; 50% examination and 50% activity in the final grade</li> </ul>	<ul style="list-style-type: none"> <li>• Two laboratory tests</li> <li>• One homework (microstrip amplifier design)</li> <li>• Continuous assesment</li> <li>• 50% in the final student's grade.</li> </ul>
8	III	Practical Training	<ul style="list-style-type: none"> <li>• According to Practice Regulation</li> </ul>	<ul style="list-style-type: none"> <li>•</li> </ul>
9	III	Electromagnetic Compatibility	<ul style="list-style-type: none"> <li>• Written exam.</li> <li>• Two internal examiners.</li> <li>• 10 subjects (8-9 theoretical and 1-2 problems). Duration: 2,5h. Weight of activity mark in the final mark: 40%.</li> <li>• A passing mark of 5 is obtained for solving half of the subjects, if the activity mark is at least 5.</li> <li>• For the maximum mark (10), 95% of the exam subjects must be properly solved and the activity mark must be at least 9,5.</li> <li>• Room assigned by the executive board of the faculty.</li> <li>• Exam subjects multiplied for each student.</li> </ul>	<ul style="list-style-type: none"> <li>• Marks of at least 5 (both for the exam and for the activity) are recognized unconditionally at any moment.</li> </ul>
10	III	Digital Switching Systems	<ul style="list-style-type: none"> <li>• Written exam.</li> <li>• Two internal examiners.</li> <li>• 10 subjects (8-9 theoretical and 1-2 problems). Duration: 2,5h. Weight of activity mark in the final mark: 40%.</li> <li>• A passing mark of 5 is obtained for solving half of the subjects, if the activity mark is at least 5.</li> <li>• For the maximum mark (10), 95% of the exam subjects must be properly solved and the activity mark must be at least 9,5.</li> </ul>	<ul style="list-style-type: none"> <li>• Marks of at least 5 (both for the exam and for the activity) are recognized unconditionally at any moment.</li> </ul>

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			<ul style="list-style-type: none"> <li>Room assigned by the executive board of the faculty.</li> <li>Exam subjects multiplied for each student.</li> </ul>	
11	III	Programmable Logic Systems	<ul style="list-style-type: none"> <li>Colocvium, 2-3 examiners, 10 questions, 50% done to pass, over 92% for 10, 2 written papers</li> </ul>	<ul style="list-style-type: none"> <li>10% bonus according to homerworks</li> <li>10% bonus accordig to innovative solutions</li> </ul>
12	III	Power Electronics	<ul style="list-style-type: none"> <li>Written examination</li> <li>Two examiners</li> <li>30 short theoretical questions and 2 problems</li> <li>Each question is evaluated by 1/3 points. Therefore the mark for evaluating the theoretical aspects equals the number of correct answers divided by 3. Each problem is evaluated on a scale from 1 to 10. The mark for the applications skills is the average of the two problems individual marks. The exam mark is the average between the mark evaluating the theoretical aspects and the mark for the applications skills.</li> <li>Requirements for minimum mark: minimum 13 correct answers from 30 and at least a mark of 5 for each problem.</li> <li>Requirements for maximum mark: 28 correct answers from 30 and at the average mark for the two problems to be at least 9.5.</li> <li>Exam room previously established by dean's office staff. The exam lasts for 3 hours with a 10 minutes break after the first 50 minutes.</li> </ul>	<ul style="list-style-type: none"> <li>En route evaluation consists of 3 marks, each mark being obtained as a result of knowledge evaluation from the previous three laboratory meetings.</li> <li>Laboratory weight is one third in the final mark, while the exam mark weight is two thirds.</li> <li>The acquired laboratory work is approved in the next academic year.</li> </ul>
13	III	Embedded Systems	<ul style="list-style-type: none"> <li>Written Evaluation</li> <li>Examination assisted by at least two instructors</li> <li>6 theoretical subjects and 3 applications for a 3 hours working time.</li> <li>Each subject has a 1 point score value. The passing score 5.</li> <li>The final grade is an average mean between written evaluation (2/3) and practical class activity (1/3) marks.</li> <li>Classroom assigned by the registrar</li> </ul>	<ul style="list-style-type: none"> <li>The following results are recognized until the graduation:</li> <li>Final Examination</li> <li>Practical class activity</li> </ul>
14	III	Digital Telephony	<ul style="list-style-type: none"> <li>Written examination, 2 hours and 30 minutes</li> <li>3 examiners</li> <li>6 equally weighted subjects each containing 1-2 theoretical questions and 1-2 numerical applications</li> <li>Grading from 1 to 10 for each subject</li> <li>The minimum mark in order to pass the examination is 5, corresponding to basic understanding of the theoretical aspects of the course and the ability of solving simple numerical applications similar to the examples presented in the course</li> <li>The maximum mark requires a detailed understanding of the theoretical aspects and the ability to solve complex applications, slightly different from those presented in the course</li> </ul>	<ul style="list-style-type: none"> <li>The course activity is graded based on a number of 7 – 8 homeworks, with grading from 1 to 10.</li> <li>The laboratory activity consists of 8 practical classes each ending with a test. The tests verify the understanding of the practical aspects and the ability to interpret the practical results</li> </ul>

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15	III	Audio and Video Systems	<ul style="list-style-type: none"> <li>Two-part written examination (usually first part in the 6th week and the second part in the 12th week), average of two examiners.</li> <li>5 questions for each examination (10 total questions).</li> <li>Grading from 0 to 10, minimum mark in order to pass each examination is 5 corresponding to general understanding and knowledge of the course and the ability to calculate course-based numerical examples; for the maximum grade the requirements consist in a detailed comprehension and knowledge of the course plus the ability to compute also tricky numerical examples (designed to evaluate a sharp comprehension of the course).</li> </ul>	<ul style="list-style-type: none"> <li>Practical activity concerning the main course topics, followed at the end of each class by a short written test (usually 10 minutes). The tests are designed to evaluate students' understanding of the practical work and of the main course topics involved. Grading from 0 to 10 for each test, minimum average required to pass the activity is 5</li> <li>Individual project due at the end of the semester. The project requirements are designed to verify the knowledge and comprehension of the practical activity of the entire semester. Grading from 0 to 10 , minimum project grade required is 5</li> </ul>
16	III	Practical Training	<ul style="list-style-type: none"> <li>According to Practice Regulation</li> </ul>	<ul style="list-style-type: none"> <li></li> </ul>
17	IV	Electronic Equipment Testing	<ul style="list-style-type: none"> <li>Written examination, 2 examiners, 6-8 theoretical questions and 1 problem;</li> <li>Condition: minimum 5 for theory and problems;</li> <li>Final mark calculated as: <math>[0,6 * Nexam + 0,4 * Nactiv + 0,5]</math>. Condition for maximum mark: from the above formula.</li> <li>Evaluation conditions have been announced at the first course and sent to the students by email.</li> </ul>	<ul style="list-style-type: none"> <li>3-4 tests during the semester, as part of the laboratory</li> <li>possibility to redo tests</li> <li>possibility to redo 2-3 laboratories</li> </ul>
18	IV	Software Development	<ul style="list-style-type: none"> <li>Colocvium, 2-3 examiners, 10 questions, 50% done to pass, over 92% for 10, 2 written papers</li> </ul>	<ul style="list-style-type: none"> <li>10% bonus according to homerworks</li> <li>10% bonus accordig to innovative solutions</li> </ul>
19	IV	Modelling and Simulation	<ul style="list-style-type: none"> <li>Written exam including only applications.</li> <li>Practical mathematical modeling and computer simulations which are held in a laboratory room and will be evaluated by tests and homework applications. The laboratory activity includes a project. The average mark of the tests, homework and project represents 1/3 of the final mark.</li> <li>Minimum 2 examiners.</li> <li>Written applications part – 5 questions.</li> <li>The mark 5 means 50% correct answers for the applications,</li> <li>The mark 10 means 100% correct answers for the applications.</li> </ul>	<ul style="list-style-type: none"> <li>Permanent oral evaluation during practical activities, tests, homework and project from the topics are covered by the laboratory.</li> <li>The grade 5 or higher for the written examination and for the activity during semester remains valid for any future</li> </ul>

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			<ul style="list-style-type: none"> <li>The mark from the written exam including applications represents 2/3 of the final mark. The written exam will be held in a course room.</li> <li>The final mark will be computed if the marks from the written exam and the practical tests are both equal or greater than 5 and will be rounded according RODPI of UPT.</li> </ul>	examination.
20	IV	VHDL	<ul style="list-style-type: none"> <li>Semester activities – 3 tests distributed over the semester, written final examination,</li> <li>2 examiners, proficiency and literacy in VHDL and simulation progress as minimum requirements, exams exigencies discussed before exam.</li> </ul>	<ul style="list-style-type: none"> <li>Presence at classes, passing all tests during the semester.</li> </ul>
21	IV	Digital Signal Processors	<ul style="list-style-type: none"> <li>Written exam.</li> <li>Two internal examiners.</li> <li>A theoretical subject (20 mcqs) and 2 to 4 problems. Duration: 2,5h. Weight of activity mark in the final mark: 33%.</li> <li>A passing mark of 5 is obtained for solving half of the subjects, if the activity mark is at least 5.</li> <li>For the maximum mark (10), 90% of the exam subjects must be properly solved and the activity mark must be at least 9,5.</li> <li>Room assigned by the executive board of the faculty. Exam subjects multiplied for each student.</li> </ul>	<ul style="list-style-type: none"> <li>The marks equal or greater than 5 for the exam and for the laboratory activity will be recognized unconditionally at any moment.</li> </ul>
22	IV	Microelectronics	<ul style="list-style-type: none"> <li>2 written examinations</li> <li>2 supervisors</li> <li>20 questions concerning theoretical knowledge (0.5 points each, maximum score is 10 points)</li> <li>Final mark: the weighted average of the examination score (2/3) and the lab activity (1/3)</li> <li>Minimum mark: minimum 5 for written examination and minimum 5 for lab activity</li> <li>Maximum mark: the weighted average equal to or greater than 9.5</li> <li>Eval. conditions setup: - According to the RODPI of UPT</li> </ul>	<ul style="list-style-type: none"> <li>During the lab activity, the teacher is testing the students on their knowledge of lab experiments (3 written tests and a final interview). The average of these 4 marks is the mark for lab activity.</li> </ul>
23	IV	Integrated Digital Networks	<ul style="list-style-type: none"> <li>Written examination, 2 hours and 30 minutes</li> <li>3 examiners</li> <li>6 equally weighted subjects each containing 1-2 theoretical questions and 1-2 numerical applications</li> <li>Grading from 1 to 10 for each subject</li> <li>The minimum mark in order to pass the examination is 5, corresponding to basic understanding of the theoretical aspects of the course and the ability of solving simple numerical applications similar to the examples presented in the course</li> <li>The maximum mark requires a detailed understanding of the theoretical aspects and the ability to solve complex applications, slightly different from those presented in the course</li> </ul>	<ul style="list-style-type: none"> <li>The course activity is graded based on a number of 3 – 4 homeworks, with grading from 1 to 10.</li> <li>The laboratory activity consists of 8 practical classes each ending with a test. The tests verify the understanding of the practical aspects and the ability to interpret the practical results</li> </ul>

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24	IV	VHDL - Project	<ul style="list-style-type: none"> <li>Semester activities – one presentation of project progress during the semester, final project presentation in place of final examination,</li> <li>proficiency in VHDL modeling, simulation proficiencies as minimum requirements, project implementation in FPGA is necessary for maximum mark, objectives and exigencies discussed at the beginning of the semester.</li> </ul>	<ul style="list-style-type: none"> <li>Project progress assessed constantly during the two periods in between results presentations.</li> </ul>
25	IV	Digital Signal Processors - Project	<ul style="list-style-type: none"> <li>Semester activities – one presentation of project progress during the semester, final project presentation in place of final examination,</li> <li>Maximum mark obtained if the final goal is attained.</li> </ul>	<ul style="list-style-type: none"> <li>Project progress assessed constantly during the two periods in between results presentations.</li> </ul>
26	IV	Digital Radio Communications	<ul style="list-style-type: none"> <li>2 Written examination, 1 hours and 15 minutes each</li> <li>3 examiners</li> <li>6 equally weighted subjects 2-3 theoretical questions and 3-4 numerical applications</li> <li>Grading from 1 to 10 for each subject</li> <li>The minimum mark in order to pass the examination is 5, corresponding to basic understanding of the theoretical aspects of the course and the ability of solving simple numerical applications similar to the examples presented in the course</li> <li>The maximum mark requires a detailed understanding of the theoretical aspects and the ability to solve complex applications, slightly different from those presented in the course</li> <li>The final mark is a weighted average between written examination (60%) and the activity during the semester (40%).</li> </ul>	<ul style="list-style-type: none"> <li>The course activity is graded based on a number of 3 – 4 homeworks, with grading from 1 to 10.</li> <li>The laboratory activity consists of 4 practical classes each ending with a test (tests verify the understanding of the practical aspects and the ability to interpret the practical results) and other 4 classes in which students must build a radio receiver, and measure the main characteristics. This part require a short report in order to explain how radio receiver work and how they measure the characteristics.</li> </ul>
27	IV	Optical Communications	<ul style="list-style-type: none"> <li>Written examination, theoretical subjects and problems, minimum mark is given for at least 50% of the maximum rating, maximum mark for at least 95% of it, if lab activity is passed.</li> </ul>	<ul style="list-style-type: none"> <li>Permanent oral evaluation during practical activities and multiple choice final test from the topics covered by the labs.</li> </ul>
28	IV	Image Processing	<ul style="list-style-type: none"> <li>Written examination, theoretical subjects and problems, minimum mark is given for at least 50% of the maximum rating, maximum mark for at least 95% of it, if lab activity is passed.</li> </ul>	<ul style="list-style-type: none"> <li>Student activity during semester is mainly evaluated at lab works and accounts for 40% of the final mark.</li> </ul>
29	IV	Biomedical Electronics	<ul style="list-style-type: none"> <li>Written examination</li> <li>Two internal supervisors;</li> <li>4 theoretical subjects, 2.25 points for each subject + 1 point from the start</li> <li>2 applications, 4.5 points for each subject + 1 point from the start</li> <li>The finale grade is calculated as sum of 2/3 of the grade for the exam and 1/3 of the grade for the activity during the semester</li> <li>It is necessary to pass (grade 5 at least) both written examination and activity during the semester to qualify for a course average calculation</li> <li>The written examination is held in an adequate classroom decided by the faculty's</li> </ul>	<ul style="list-style-type: none"> <li>Permanent oral evaluation during practical activities and multiple choice final test from the topics covered by the labs.</li> <li>The grade 5 or higher for the written examination and for the activity during semester remains valid for any next examination.</li> </ul>

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			executive board	
30	IV	Electronic Packaging	<ul style="list-style-type: none"> <li>• Written examination with 2 examiners and minimum 20 questions</li> <li>• The examination will check the general knowledge acquired in the field of electronic packaging through a large number of questions with short answers.</li> <li>• The students will also be asked one or two questions requiring detailed answers, either synthesis or focused on a specific topic.</li> <li>• The requirement for minimum mark is to answer 70% of the short questions. The requirement for maximum mark is to answer all the questions</li> </ul>	<ul style="list-style-type: none"> <li>• The students are required to participate, during the semester, at minimum 50% of the course and 90% of the laboratory classes.</li> <li>• Each laboratory work will be graded by the teacher based on a short test at the end of the class, covering the topics discussed during the session</li> <li>• During the semester the students will be asked to complete and present a project that will be graded at the end of the semester.</li> </ul>
31	IV	Wireless Communications	<ul style="list-style-type: none"> <li>• Written examination in one session, with 2 examiners.</li> <li>• 10-12 questions, including 7-8 for theoretical knowledge, and 2-3 problems.</li> <li>• Final mark : <math>\frac{2}{3} * (\frac{1}{3} \text{ theoretical knowledge} + \frac{2}{3} \text{ problems solving}) + \frac{1}{3} \text{ seminar activity}</math></li> <li>• Mark 5 : minimum 5 at theoretical knowledge, minimum 5 at problems solving and minimum 5 at seminar activity</li> <li>• Mark 10 : arithmetical mean greater than 9.5.</li> <li>• Provided by the faculty.</li> </ul>	<ul style="list-style-type: none"> <li>• The grade for the activity during the semester represents the simple average of the laboratory grade. The laboratory grade reflects the practical skills of the student, and the results obtained by short tests.</li> <li>• The grade 5 or higher for the written exam or for the activity during the semester, remains valid for any next examination.</li> </ul>
32	IV	Software Project	<ul style="list-style-type: none"> <li>• Progress grades during the semester; final demo and evaluation</li> </ul>	<ul style="list-style-type: none"> <li>• 10% bonus accordig to innovative solutions</li> </ul>